

On the Fundamental Limits of Cache-Aided Multiuser Private Information Retrieval

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Abstract—We consider the problem of cache-aided Multiuser Private Information Retrieval (MuPIR) which is an extension of the single-user cache-aided PIR problem to the case of multiple users. In cache-aided MuPIR, each of the K_u cache-equipped users wishes to privately retrieve a message out of K messages from N databases each having access to the entire message library. Demand privacy requires that any individual database learns nothing about the demands of all users. The users are connected to each database via an error-free shared-link. In this paper, we aim to characterize the optimal trade-off between user cache memory and communication load for such systems. First, we propose a novel approach of *cache-aided interference alignment (CIA)*, for the MuPIR problem with $K = 2$ messages, $K_u = 2$ users and $N \geq 2$ databases. The CIA approach is optimal when the cache placement is uncoded. For general cache placement, the CIA approach is optimal when $N = 2$ and 3 verified by the computer-aided converse approach. Second, for the general case, we propose a *product design (PD)* which incorporates the PIR code into the linear caching code. The product design is shown to be order optimal within a multiplicative factor of 8 and is exactly optimal in the high memory regime.

Index Terms—Private information retrieval, coded caching, interference alignment, multiuser.

I. INTRODUCTION

INTRODUCED by Chor *et al.* in [3], the problem of private information retrieval (PIR) seeks efficient ways

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for a user to retrieve a desired message from N databases, each holding a library of K messages, while keeping the desired message's identity private from each database. Sun and Jafar (SJ) recently characterized the capacity of the PIR problem with non-colluding databases [4], [5]. Coded caching was originally proposed by Maddah-Ali and Niesen (MAN) in [6] for a shared-link caching network consisting of a server, which is connected to K_u users through a noiseless broadcast channel and has access to a library of K equal-length files. Each user can cache M files and requests one file. The MAN scheme proposed a combinatorial cache placement design so that during the delivery phase, each transmitted coded message is simultaneously useful to multiple users such that the communication load can be significantly reduced. Under the constraint of uncoded cache placement and for worst-case load, the MAN scheme was proved to be optimal when the number of files is no less than the number of users [7] and order optimal within a factor of two in general [8].

The combination of privacy and caching, sometimes referred to as side information, has gained significant attentions recently. Two different privacy models are commonly considered. First, in [9]–[17], the *user-against-database* privacy model was studied where individual databases are prevented from learning the single user's demand. The author in [9] studied the case where a single cache-aided user is connected to a set of N replicated databases and showed that memory-sharing between the memory-load¹ pairs $(0, 1 + \frac{1}{N} + \dots + \frac{1}{N^{K-1}})$ and $(K, 0)$ (i.e., split the messages and cache memories proportionally and then implement two PIR schemes on the two independent parts of the messages) is actually optimal if the databases are aware of the users' cached content. However, if the databases are unaware of the user's cached content, then there is a multiplicative “unawareness gain” in capacity in terms of the user memory as shown in [10], [11]. Different from [9]–[11] where the cache can be arbitrary functions of the messages, the user's cache is restricted to be in the form of M full messages in [12]–[18]. Along this line, two models are usually studied, referred to as PIR-SI (PIR with non-private side information) where the cached messages are not known by the databases but the privacy of the cache needs not to be preserved, and PIR-PSI (PIR with private side information) where the joint privacy of both the desired message and the cache needs to be preserved. For PIR-SI with a single database ($N = 1$), the capacity is shown

¹The capacity of PIR is defined as the inverse of the minimum load.

to be $\left\lceil \frac{K}{M+1} \right\rceil^{-1}$ [12]. For the case of multiple databases ($N \geq 2$), [12] provided an achievable scheme achieving the load $1 + \frac{1}{N} + \dots + \frac{1}{\left\lceil \frac{K}{M+1} \right\rceil^{-1}}$, which was later shown to be optimal in [18]. For PIR-PSI with a single database, [12] showed that the capacity is $(K - M)^{-1}$, implying that the impact of private side information is equivalent to reducing the message library size by M . This effect is also seen in [19] which showed that the capacity of PIR-PSI with arbitrary number of databases is $(1 + \frac{1}{N} + \dots + \frac{1}{N^{K-M-1}})^{-1}$. Second, the authors in [20]–[23] considered the *user-against-user* privacy model where users are prevented from learning each other's demands. The authors in [20] first formulated the *coded caching with private demands* problem where a shared-link caching network with demand privacy, i.e., any user cannot learn anything about the demands of other users, was considered. The goal is to design efficient delivery schemes such that the communication load is minimized while preserving such privacy. Order optimal schemes were proposed based on the concept of virtual user.

This paper formulates the problem of cache-aided Multiuser PIR (MuPIR), where each of the K_u cache-equipped users aims to retrieve a message from N distributed non-colluding databases while preventing any one of them from gaining knowledge about the user demands given that the cached content of the users are known to the databases.² The contribution of this paper includes: First, based on the novel idea of *cache-aided interference alignment (CIA)*, we construct cache placement and private delivery schemes achieving the memory-load pairs $(\frac{N-1}{2N}, \frac{N+1}{N})$ and $(\frac{2(N-1)}{2N-1}, \frac{N+1}{2N-1})$ for the case of $K = 2$ messages, $K_u = 2$ users and $N \geq 2$ databases. Different from the existing cache-aided interference alignment schemes in [26]–[28] which were designed for the cache-aided interference channels, the purpose of our proposed private delivery scheme is to let each server send symmetric messages (in order to preserve user demand privacy), each of which contains some uncached and undesired symbols (i.e., interference) for each user. The proposed CIA approach effectively aligns these interference for each user and thus facilitates correct decoding. We prove that the proposed scheme is optimal under the constraint of uncoded cache placement. Computer-aided investigation given in [29] also shows that the proposed schemes are optimal for general cache placement when $N = 2$ and 3. Second, for general system parameters K, K_u and N , we propose a *Product Design (PD)* which incorporates the SJ scheme [5] into the MAN coded caching scheme [6]. Interestingly, the load of the proposed design is the product of the loads achieved by these two schemes and is optimal within a factor of 8. Moreover, PD is exactly optimal in the high memory regime. Finally, we characterize the optimal memory-load trade-off for the case of $K = K_u = N = 2$ where the users demand distinct messages. It is shown that

²Note that the virtual user strategy and the strategy based on scalar linear function retrieval for coded caching with private demands [20], [24], [25] were designed based on the fact that the user caches are not transparent to each other (but transparent to the single server). However, such an approach which relies on the unawareness of cache to achieve demand privacy cannot be used in the considered MuPIR problem because the databases are aware of the user caches.

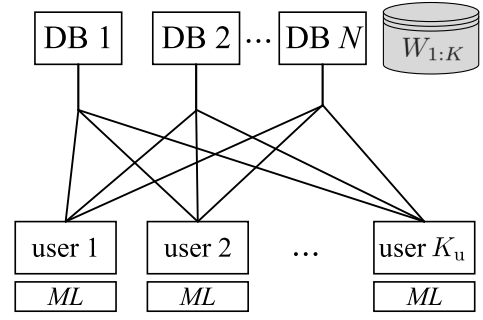


Fig. 1. Cache-aided MuPIR system with N replicated databases, K independent messages and K_u cache-equipped users. The users are connected to each DB via an error-free shared-link broadcast channel.

under the constraint of the distinct demands, the optimal load can be strictly smaller than the case with general demands.

The paper is organized as follows. In Section II, we give a formal description of the problem setup. The main results are given in Section III. In Section IV, we present the proposed CIA based schemes and in Section V, we present the product design for general system parameters. We discuss some interesting observations for the case of distinct demands in Section VI. Finally, we conclude this paper and provide several future directions in Section VII.

Notation Convention: \mathbb{Z}^+ denotes the non-negative integer set. $[n] \triangleq \{1, 2, \dots, n\}$, $[m : n] \triangleq \{m, m+1, \dots, n\}$ and $(m : n) \triangleq (m, m+1, \dots, n)$ for some $m \leq n$. For two sets \mathcal{A} and \mathcal{B} , the *difference set* is defined as $\mathcal{A} \setminus \mathcal{B} \triangleq \{x \in \mathcal{A} : x \notin \mathcal{B}\}$. For an index set \mathcal{I} , denote $A_{\mathcal{I}} \triangleq \{A_i : i \in \mathcal{I}\}$. If $\mathcal{I} = [m : n]$, we write $A_{[m:n]}$ as $A_{m:n}$ for simplicity. For an index vector $I = (i_1, \dots, i_n)$, denote $A_I \triangleq (A_{i_1}, \dots, A_{i_n})$. Let $\mathbf{0}_n \triangleq (0, \dots, 0)$ and $\mathbf{1}_n \triangleq (1, \dots, 1)$ with length n . \mathbf{I}_n denotes the identity matrix of order n . For a matrix \mathbf{A} , $\mathbf{A}(i, :)$ and $\mathbf{A}(:, j)$ denote the i -th row and j -th column of \mathbf{A} respectively. \mathbf{A}^T represents the transpose of \mathbf{A} . Operations are on the binary field.

II. PROBLEM FORMULATION

We consider a system with K_u users, each aiming to privately retrieve a message from $N \geq 2$ replicated non-colluding databases (DBs). Each DB stores K independent messages, denoted by W_1, \dots, W_K , each of which is uniformly distributed over $[2^L]$. Each user is equipped with a cache memory of size ML ($0 \leq M \leq K$) bits. Let the random variables Z_1, \dots, Z_{K_u} denote the cached content of the users. The system operates in two phases, a *cache placement phase* followed by a *private delivery phase*. In the cache placement phase, the users fill up their cache memory without the knowledge of their future demands. The cached content of each user is a function of $W_{1:K}$ and is assumed to be *known to all DBs*. In the private delivery phase, each user $k \in [K_u]$ wishes to retrieve a message W_{θ_k} where $\theta_k \in [K]$. Let $\boldsymbol{\theta} \triangleq (\theta_1, \dots, \theta_{K_u})$ be the demand vector which represents the demands of the users. We assume that $\boldsymbol{\theta}$ follows an arbitrary distribution with full support over $[K]^{K_u}$. Depending on $\boldsymbol{\theta}$ and Z_1, \dots, Z_{K_u} , the users cooperatively generate N queries

$Q_1^{[\theta]}, \dots, Q_N^{[\theta]}$, and then send the n -th query $Q_n^{[\theta]}$ to DB n . Upon receiving the query, DB n responds with an answer $A_n^{[\theta]}$ broadcasted to all users. The answer $A_n^{[\theta]}$ is a deterministic function of $Q_n^{[\theta]}$, $W_{1:K}$ and $Z_{1:K_u}$, which written in terms of conditional entropy, is

$$H(A_n^{[\theta]} | Q_n^{[\theta]}, W_{1:K}, Z_{1:K_u}) = 0, \quad \forall n \in [N]. \quad (1)$$

After collecting all the answers from the DBs, the users can correctly recover their desired messages with the help of their cached information, i.e.,

$$H(W_{\theta_k} | Q_{1:N}^{[\theta]}, A_{1:N}^{[\theta]}, Z_k) = 0, \quad \forall k \in [K_u]. \quad (2)$$

To preserve the privacy with respect to the DBs, it is required that³

$$I(\theta; Q_n^{[\theta]}, A_n^{[\theta]}, W_{1:K}, Z_{1:K_u}) = 0, \quad \forall n \in [N]. \quad (3)$$

Let D denote the total number of bits broadcasted from the DBs, then the *load* of the cache-aided MuPIR problem is defined as⁴

$$R \triangleq \frac{D}{L} = \frac{\sum_{n=1}^N H(A_n^{[\theta]})}{L}. \quad (4)$$

From the privacy constraint (3), the load can also be written as $R = \frac{1}{L} \sum_{n=1}^N H(A_n^{[\theta^i]})$, $\forall i \in [K^{K_u}]$ where θ^i represents the i -th realization of all the K^{K_u} possible realizations of the demand vector. This is because the load R should not depend on the user demands θ otherwise it leaks information about the user demands to the DBs and (3) will be violated.

A memory-load pair (M, R) is said to be achievable if there exists a MuPIR scheme satisfying the decodability constraint (2) and the privacy constraint (3). The goal of the MuPIR problem is to design the cache placement and the corresponding private delivery phases such that the load is minimized. For any $0 \leq M \leq K$, let $R^*(M)$ denote the minimal achievable load. In addition, if the users directly cache a subset of the library bits, the placement phase is said to be *uncoded*. We denote the minimum achievable load under the constraint of uncoded cache placement by $R_{\text{uncoded}}^*(M)$. Note that any converse bound $R'(M)$ on the worst-case load for the (K, K_u) coded caching problem without considering demand privacy formulated in [6] is also a converse bound on $R^*(M)$, i.e., $R^*(M) = \frac{1}{L} \sum_{n=1}^N H(A_n^{[\theta^1]}) \geq R'(M)$. Similarly, any converse bound $R''(M)$ on the worst-case caching load under the constraint of uncoded cache placement is also a converse on $R_{\text{uncoded}}^*(M)$, i.e., $R_{\text{uncoded}}^*(M) \geq R''(M)$.

III. MAIN RESULTS

First, we consider the MuPIR problem with $K = K_u = 2$ and $N \geq 2$. In this case, we propose a novel *cache-aided interference alignment* (CIA) based scheme (see Section IV) and the corresponding achievable load is given in Theorem 1.

³The privacy constraint (3) can be equivalently written as $I(\theta; Q_n^{[\theta]}, W_{1:K}, Z_{1:K}) = 0, \forall n \in [N]$ since the answer $A_n^{[\theta]}$ is a deterministic function of $Q_n^{[\theta]}$ and $W_{1:K}$.

⁴Note that in the literature of PIR, R denotes the retrieval *rate*, which is the number of useful bits per bit of download in the single-user case. Our definition of load is consistent with the caching literature, representing the (normalized) number of bits transmitted through the shared-link.

Theorem 1: For the cache-aided MuPIR problem with $K = 2$ messages, $K_u = 2$ users and $N \geq 2$ DBs, the following load is achievable

$$R_{\text{CIA}}(M) = \begin{cases} 2(1-M), & 0 \leq M \leq \frac{N-1}{2N} \\ \frac{(N+1)(3-2M)}{2N+1}, & \frac{N-1}{2N} \leq M \leq \frac{2(N-1)}{2N-1} \\ \left(1 - \frac{M}{2}\right) \left(1 + \frac{1}{N}\right), & \frac{2(N-1)}{2N-1} \leq M \leq 2 \end{cases} \quad (5)$$

Proof: The proof of Theorem 1 is provided in Section IV, where we present the CIA based approach achieving the memory-load pairs $(\frac{N-1}{2N}, \frac{N+1}{N})$ and $(\frac{2(N-1)}{2N-1}, \frac{N+1}{2N-1})$. Together with the two trivial pairs $(0, 2)$ and $(2, 0)$, we obtain four corner points. By the memory-sharing among these corner points, the load in Theorem 1 can be achieved. ■

Remark 1: The computer-aided approach given in [29] shows that the achievability result in Theorem 1 is optimal when $N = 2, 3$. For $N \geq 4$, the converse remains open. In addition, the achieved load by the CIA based scheme is better than applying twice the single-user cache-aided PIR of [9] for each user, which yields a load of $(2-M)(1 + \frac{1}{N}) \geq R_{\text{CIA}}(M), \forall M \in [0, 2]$.

Corollary 1: The load $R_{\text{CIA}}(M)$ in Theorem 1 is optimal when $M \in [0, \frac{N-1}{2N}] \cup [\frac{2(N-1)}{2N-1}, 2]$.

Proof: When $M \leq \frac{N-1}{2N}$, $R_{\text{CIA}}(M) = 2(1-M)$ coincides with the coded caching converse [6] and therefore is optimal. When $M \geq \frac{2(N-1)}{2N-1}$, $R_{\text{CIA}}(M) = (1 - \frac{M}{2})(1 + \frac{1}{N})$ coincides with the converse bound $R_{\text{single}} = (1 - \frac{M}{K})(1 + \frac{1}{N} + \dots + \frac{1}{N^{K-1}})$ for the single-user cache-aided PIR in [9]. Since increasing the number of users K_u cannot decrease the load, the achieved load in Theorem 1 is optimal. ■

Corollary 2: For the cache-aided MuPIR problem with $K = 2$ messages, $K_u = 2$ users and $N \geq 2$ DBs, the optimal memory-load trade-off under uncoded cache placement is characterized as

$$R_{\text{uncoded}}^*(M) = \begin{cases} 2 - \frac{3}{2}M, & 0 \leq M \leq \frac{2(N-1)}{2N-1} \\ \left(1 - \frac{M}{2}\right) \left(1 + \frac{1}{N}\right), & \frac{2(N-1)}{2N-1} \leq M \leq 2 \end{cases} \quad (6)$$

Proof: For achievability, the corner points in Corollary 2 are the memory-load pairs $(0, 2)$, $(\frac{2(N-1)}{2N-1}, \frac{N+1}{2N-1})$, and $(2, 0)$, which can be achieved by the same scheme as Theorem 1. It can be seen in Section IV that the achievable schemes for these corner points are uncoded, and by memory-sharing, the load of Corollary 2 can be achieved.

Under the assumption of uncoded cache placement, as shown in Section II, the converse bound for the shared-link

coded caching problem without privacy constraint in [30], [31] is also a converse for our considered cache-aided MuPIR problem. When $0 \leq M \leq 1$, it was proved that $R_{\text{uncoded}}^*(M) \geq 2 - \frac{3}{2}M$. In addition, by the single-user cache-aided PIR converse in [9], we have $R_{\text{uncoded}}^*(M) \geq (1 - \frac{M}{2})(1 + \frac{1}{N})$. Since the achievability and converse match, the optimal trade-off in Corollary 2 is characterized. ■

For general K , K_u and N , we propose an achievable scheme called *Product Design* (PD). The corresponding achievable load is given by the following theorem.

Theorem 2: The proposed product design achieves the load of $R_{\text{PD}}(M) = \min \{K - M, \hat{R}(M)\}$ in which

$$\hat{R}(M) = \frac{K_u - t}{t + 1} \left(1 + \frac{1}{N} + \cdots + \frac{1}{N^{K-1}} \right), \quad (7)$$

where $t \triangleq \frac{K_u M}{K} \in [1 : K_u]$. When $M = 0$, the load $R_0 \triangleq \min \{K_u R_{\text{MPIR}}(K, K_u, N)^{-1}, K\}$ is achievable if $K_u < K$, where $R_{\text{MPIR}}(K, K_u, N)$ represents the achievable sum rate in [32] for the Multi-message PIR (MPIR) problem with K messages, N DBs and the user desires K_u messages. For non-integer values of t , the lower convex envelope the integer points $(0, R_0)$, $(\frac{tK}{K_u}, R_{\text{PD}}(M))$, $t \in [1 : K_u]$ are achievable. Moreover, $\frac{R_{\text{PD}}(M)}{R^*(M)} \leq 8$.

Proof:

Achievability: See Section V for the schemes to achieve $\hat{R}(M)$. The load $K - M$ can be achieved by letting each user store the same $\frac{M}{K}$ fraction of all messages and then downloading the remaining $1 - \frac{M}{K}$ fraction of all messages in the delivery phase, achieving the load $K(1 - \frac{M}{K}) = K - M$. Each user can recover all the K messages including the desired one. Since the above delivery does not depend on θ , it is private. When $K > K_u$ and $M = 0$, i.e., the users do not have any cache, the considered cache-aided MuPIR problem reduces to the MPIR problem, where a superuser requests K_u out of K messages without leaking the identities the desired messages. The MPIR scheme proposed in [32] achieving the load R_0 , can be used to improve PD based on the observation that *a joint retrieval of multiple messages is better than repeating the single-message retrieval multiple times*. Therefore, the MPIR scheme of [32] can be used when $M = 0$ to achieve the load $R_0 = K_u R_{\text{MPIR}}(K, K_u, N)^{-1}$, which is strictly better than repeating the single-message retrieval scheme K_u times by plugging $t = 0$ into (7).

Converse: We use the converse bound in [33] on the worst-case load for coded caching without privacy constraint, denoted by $R_{\text{caching}}(M)$. As shown in Section II, $R_{\text{caching}}(M)$ is also a converse bound for the considered MuPIR problem. In addition, it was proved in [33] that $R_{\text{caching}}(M)$ is no less than the lower convex envelope of $\frac{1}{4} \min \{ \frac{K_u - t}{t+1}, K(1 - \frac{M}{K}) \}$ where $t \in [K_u]$. Hence, we have

$$\frac{R_{\text{PD}}(M)}{R^*(M)} \leq \frac{R_{\text{PD}}(M)}{R_{\text{caching}}(M)} \leq 4 \cdot \left(1 + \frac{1}{N} + \cdots + \frac{1}{N^{K-1}} \right) \leq 8, \quad (8)$$

which is due to $R_{\text{PD}}(M) = \min \{K(1 - \frac{M}{K}), \hat{R}(M)\}$ and $1 + \frac{1}{N} + \cdots + \frac{1}{N^{K-1}} \leq 2, \forall N \geq 2$. ■

Corollary 3: The proposed product design is optimal when $M \in \left[\frac{(K_u - 1)}{K_u} K, K \right]$.

Proof: When $K \geq K_u$ and $M = \frac{K(K_u - 1)}{K_u}$, (7) becomes $\hat{R}(M) = \frac{1}{K_u} (1 + \cdots + \frac{1}{N^{K-1}})$. On the other hand, the author in [9] showed that when $K_u = 1$, the optimal single-user cache-aided PIR load is equal to $(1 - \frac{M}{K})(1 + \cdots + \frac{1}{N^{K-1}})$, which also equals (7) when $M = \frac{K(K_u - 1)}{K_u}$. By the memory-sharing between $(\frac{K(K_u - 1)}{K_u}, \hat{R}(\frac{K(K_u - 1)}{K_u}))$ and $(K, 0)$, we conclude that PD is optimal when $\frac{(K_u - 1)}{K_u} K \leq M \leq K$. ■

Numerical Evaluation: In Fig. 2, we consider the MuPIR systems with $K = K_u = 2$, where $N = 2$ in Fig. 2a and $N = 3$ in Fig. 2b respectively. We compare the proposed CIA based scheme in Theorem 1, the optimal scheme under uncoded cache placement in Corollary 6, the product design in Theorem 2, and the computer-aided converse in [29]. In addition, it will be clarified in Theorem 3 that for the case $K = K_u = N = 2$, if the users demand distinct messages, the optimal memory-load trade-off is $R_d^*(M)$ given in (43). In Fig. 2a, there are two non-trivial corner points $(1/4, 3/2)$ and $(2/3, 1)$ associated with the CIA based scheme in Theorem 1. It can be seen that in the case of general demands, the CIA based scheme outperforms both the optimal scheme under uncoded cache placement in Corollary 2 and the product design in Theorem 2. When $1/4 \leq M \leq 2$, $R_{\text{CIA}}(M)$ coincides with the computer-aided converse [29] and hence is optimal. It also can be seen that a lower load can be achieved when users only have distinct demands. Fig. 2b shows the case when $N = 3$ in which $R_{\text{CIA}}(M)$ is optimal when $1/3 \leq M \leq 2$ by the computer-aided converse. In Fig. 3, we compare the load of the product design with the best-known caching bound provided in [8] when $K = K_u = 6, N = 2$. More specifically, Theorem 2 of [8] gives the caching converse as a lower convex envelope of the set of memory-load pairs $\{(\frac{7-\ell}{s}, \frac{s-1}{2} + \frac{\ell(\ell-1)}{2s}) : \forall s \in [1 : 6], \forall \ell \in [1 : s]\} \cup \{(0, 6)\}$. Since $R_{\text{PD}}(1)$ lies above the line segment connecting the memory-load pairs $(0, 6)$ and $(2, R_{\text{PD}}(2))$, we can use memory-sharing to achieve a better load for $M = 1$.

IV. PROOF OF THEOREM 1: DESCRIPTION OF THE CIA SCHEME

In this section, we prove Theorem 1. For the cache-aided MuPIR problem with $K = 2$ messages, $K_u = 2$ users and $N \geq 2$ DBs, we first show the achievability of the memory-load pairs $(\frac{N-1}{2N}, \frac{N+1}{N})$ and $(\frac{2(N-1)}{2N-1}, \frac{N+1}{2N-1})$ using the proposed CIA scheme. Note that when $M = 0$, we let any one of the DBs broadcast the two messages to the users, so the memory-load pair $(0, 2)$ is achievable. When $M = 2$, we let both users store the two messages in the placement phase and there is no need for the DBs to transmit anything, implying that $(2, 0)$ is achievable. By the memory-sharing between the above four corner points, the load of Theorem 1 can be achieved. For each of the above two non-trivial corner points, we first describe the general achievable schemes for arbitrary number of DBs and then present an example to highlight the design

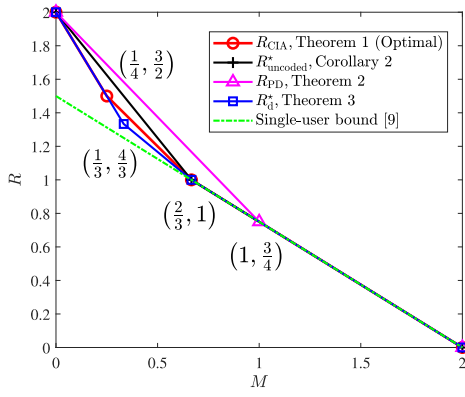
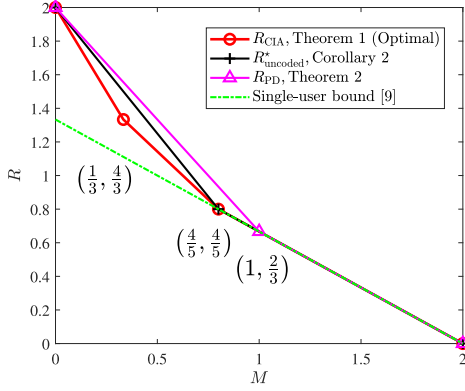
(a) $K = K_u = 2, N = 2$.(b) $K = K_u = 2, N = 3$

Fig. 2. An illustration of the achievable load R of the MuPIR system with $K = K_u = 2$. (a) $N = 2$. Both the CIA scheme (R_{CIA}) and the optimal scheme under distinct demands ($R_d^*(M)$) have four corner points; Both the optimal scheme under uncoded cache placement ($R_{uncoded}^*$) and the product design (R_{PD}) have three corner points; (b) $N = 3$. R_{CIA} has four corner points. $R_{uncoded}^*$ and R_{PD} have three corner points.

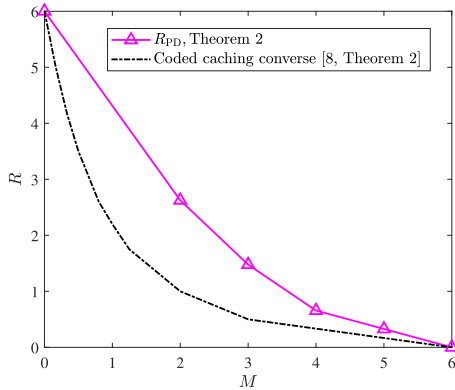


Fig. 3. The load of the product design compared to the best-known caching bound [8] for $K = K_u = 6, N = 2$.

intuition. Computer-aided investigation [29] shows that the achievable load in Theorem 1 is optimal when $N = 2$ and 3. For general values of N , the converse remains open.

A. Achievability of $(\frac{N-1}{2N}, \frac{N+1}{N})$

Let $W_1 = A$ and $W_2 = B$ denote the two messages, each consisting of $L = 2N$ bits, i.e., $A = (A_1, \dots, A_{2N}), B = (B_1, \dots, B_{2N})$. The proposed scheme is described as follows.

1) *Cache Placement*: Each user stores $N - 1$ linear combinations of the message bits in its cache (therefore $M = \frac{N-1}{2N}$), i.e.,

$$Z_1 = \{\alpha_{1,j}A_{(1:N)}^\top + \beta_{1,j}B_{(1:N)}^\top : j \in [N-1]\}, \quad (9a)$$

$$Z_2 = \{\alpha_{2,j}A_{(N+1:2N)}^\top + \beta_{2,j}B_{(N+1:2N)}^\top : j \in [N-1]\}, \quad (9b)$$

in which the linear combination coefficients $\alpha_{i,j}, \beta_{i,j} \in \mathbb{F}_2^{1 \times N} \setminus \{\mathbf{0}_N\}, \forall i \in [2], \forall j \in [N-1]$ are chosen such that $\text{rank}([\alpha_{i,1}; \dots; \alpha_{i,N-1}]) = N-1$ and $\text{rank}([\beta_{i,1}; \dots; \beta_{i,N-1}]) = N-1, \forall i \in [2]$. WLOG, we choose the coefficients to be $[\alpha_{i,1}; \dots; \alpha_{i,N-1}] = [\beta_{i,1}; \dots; \beta_{i,N-1}] = [\mathbf{I}_{N-1}, \mathbf{0}_N^\top], \forall i \in [2]$. Recall that $A_{(1:N)} \triangleq (A_1, \dots, A_N)$ and other notations follow similarly. Furthermore, let $Z_{i,j}$ denote the j -th linear combination in Z_i , i.e., $Z_{1,j} = \alpha_{1,j}A_{(1:N)}^\top + \beta_{1,j}B_{(1:N)}^\top, Z_{2,j} = \alpha_{2,j}A_{(N+1:2N)}^\top + \beta_{2,j}B_{(N+1:2N)}^\top, \forall j \in [N-1], \forall i \in [2]$.

2) *Private Delivery*: In this phase, the two users download an answer from each DB according to their demands (θ_1, θ_2) . The answers are in the form of random linear combinations of certain message bits. In particular, the answer of DB $n \in [N-1]$ consists of two random linear combinations, i.e., $A_n^{[\theta]} \triangleq (A_{n,1}^{[\theta]}, A_{n,2}^{[\theta]})$ where $A_{n,1}^{[\theta]} = \mathbf{u}_{n,1}A_{(1:N)}^\top + \mathbf{v}_{n,1}B_{(1:N)}^\top, A_{n,2}^{[\theta]} = \mathbf{u}_{n,2}A_{(N+1:2N)}^\top + \mathbf{v}_{n,2}B_{(N+1:2N)}^\top$. The answer of DB N consists of four random linear combinations, i.e., $A_N^{[\theta]} \triangleq (A_{N,1}^{[\theta]}, A_{N,2}^{[\theta]}, A_{N,3}^{[\theta]}, A_{N,4}^{[\theta]})$ where $A_{N,1}^{[\theta]} = \mathbf{g}_1A_{(1:N)}^\top, A_{N,2}^{[\theta]} = \mathbf{g}_2B_{(1:N)}^\top, A_{N,3}^{[\theta]} = \mathbf{g}_3A_{(N+1:2N)}^\top$ and $A_{N,4}^{[\theta]} = \mathbf{g}_4B_{(N+1:2N)}^\top$. The coefficient vectors $\mathbf{g}_1, \mathbf{g}_2, \mathbf{g}_3, \mathbf{g}_4, \mathbf{u}_{n,j}, \mathbf{v}_{n,j} \in \mathbb{F}_2^{1 \times N}, \forall n \in [N-1], \forall j \in [2]$ used in the answers are subject to design according to the user demands. Therefore, $2N+2$ linear combinations will be downloaded in total in the delivery phase. The answers can be written as

$$\begin{bmatrix} A_{1,1}^{[\theta]} \\ A_{1,2}^{[\theta]} \\ \vdots \\ A_{N-1,1}^{[\theta]} \\ A_{N-1,2}^{[\theta]} \\ A_{N,1}^{[\theta]} \\ A_{N,2}^{[\theta]} \\ A_{N,3}^{[\theta]} \\ A_{N,4}^{[\theta]} \end{bmatrix} = \begin{bmatrix} \mathbf{u}_{1,1} & \mathbf{0}_N & \mathbf{v}_{1,1} & \mathbf{0}_N \\ \mathbf{0}_N & \mathbf{u}_{1,2} & \mathbf{0}_N & \mathbf{v}_{1,2} \\ \vdots & \vdots & \vdots & \vdots \\ \mathbf{u}_{N-1,1} & \mathbf{0}_N & \mathbf{v}_{N-1,1} & \mathbf{0}_N \\ \mathbf{0}_N & \mathbf{u}_{N-1,2} & \mathbf{0}_N & \mathbf{v}_{N-1,2} \\ \mathbf{g}_1 & \mathbf{0}_N & \mathbf{0}_N & \mathbf{0}_N \\ \mathbf{0}_N & \mathbf{0}_N & \mathbf{g}_2 & \mathbf{0}_N \\ \mathbf{0}_N & \mathbf{g}_3 & \mathbf{0}_N & \mathbf{0}_N \\ \mathbf{0}_N & \mathbf{0}_N & \mathbf{0}_N & \mathbf{g}_4 \end{bmatrix}$$

$$\times \begin{bmatrix} A_{(1:N)}^\top \\ A_{(N+1:2N)}^\top \\ B_{(1:N)}^\top \\ B_{(N+1:2N)}^\top \end{bmatrix}. \quad (10)$$

We next show how the linear coefficients can be designed using the idea of CIA such that the users can correctly recover their desired messages. Due to space limit, we will only consider $(\theta_1, \theta_2) = (1, 2)$ and $(1, 1)$. Other cases work similarly and are omitted here.

For $(\theta_1, \theta_2) = (1, 2)$, i.e., user 1 and 2 demand messages A and B respectively, the following six N -by- N coefficient matrices should be full-rank:

Full-rank condition: the following matrices are full-rank,

$$\underbrace{\begin{bmatrix} \alpha_{1,1} \\ \vdots \\ \alpha_{1,N-1} \\ \mathbf{g}_1 \end{bmatrix}, \begin{bmatrix} \beta_{2,1} \\ \vdots \\ \beta_{2,N-1} \\ \mathbf{g}_4 \end{bmatrix}, \begin{bmatrix} \mathbf{u}_{1,2} \\ \vdots \\ \mathbf{u}_{N-1,2} \\ \mathbf{g}_3 \end{bmatrix}, \begin{bmatrix} \mathbf{v}_{1,1} \\ \vdots \\ \mathbf{v}_{N-1,1} \\ \mathbf{g}_2 \end{bmatrix}}_{\text{For correct decoding of } (\theta_1, \theta_2) = (1, 2)}, \quad (11)$$

$$\underbrace{\begin{bmatrix} \alpha_{2,1} \\ \vdots \\ \alpha_{2,N-1} \\ \mathbf{g}_3 \end{bmatrix}, \begin{bmatrix} \beta_{1,1} \\ \vdots \\ \beta_{1,N-1} \\ \mathbf{g}_2 \end{bmatrix}}_{\text{For privacy}}.$$

Note that only the first four coefficient matrices being full-rank in (11) is mandatory for the decoding of the demands $(\theta_1, \theta_2) = (1, 2)$. The two extra matrices $[\alpha_{2,1}; \dots; \alpha_{2,N-1}; \mathbf{g}_3]$ and $[\beta_{1,1}; \dots; \beta_{1,N-1}; \mathbf{g}_2]$ being full-rank is mandatory for the decoding of other user demands. The reason that we require the two extra matrices to be full-rank for $(\theta_1, \theta_2) = (1, 2)$ is that, if the two matrices are not full-rank here (the DBs can check this since the users' cache coefficients $\alpha_{2,n}, \beta_{1,n}, \forall n \in [N-1]$ are known to the DBs), then DB 2 can know that the actual demands being requested are $(\theta_1, \theta_2) = (1, 2)$ since correct decoding is impossible for any demands other than $(1, 2)$. In fact, to preserve privacy, any full-rank coefficient matrix consisting of the linear coefficients of one DB and the cache coefficients which are necessary for the correct decoding of one demand vector (θ_1, θ_2) must be full-rank for all possible demands $(\theta_1, \theta_2) \in [2]^2$. This multi-purpose full-rank requirement holds for all user demands. The required alignment is

$$\text{Alignment condition: } \mathbf{g}_1 = \mathbf{u}_{n,1}, \quad \forall n \in [N-1], \quad (12a)$$

$$\mathbf{g}_4 = \mathbf{v}_{n,2}, \quad \forall n \in [N-1]. \quad (12b)$$

We next show that with the above full-rank and alignment conditions, the two users can correctly recover the messages A and B respectively.

Due to the alignment condition of (12b), we have $A_{N,4}^{[(1,2)]} = \mathbf{g}_4 B_{(N+1:2N)}^\top = \mathbf{v}_{n,2} B_{(N+1:2N)}^\top, \forall n \in [N-1]$, i.e., the message bits $B_{(N+1:2N)}$ are aligned among the linear combinations $A_{1,2}^{[(1,2)]}, \dots, A_{N-1,2}^{[(1,2)]}$. Subtracting $A_{N,4}^{[(1,2)]}$ from $A_{1,2}^{[(1,2)]}, \dots, A_{N-1,2}^{[(1,2)]}$ in (10), we obtain

$$\begin{bmatrix} A_{1,2}^{[(1,2)]} - A_{N,4}^{[(1,2)]} \\ \vdots \\ A_{N-1,2}^{[(1,2)]} - A_{N,4}^{[(1,2)]} \\ A_{N,3}^{[(1,2)]} \end{bmatrix} = \begin{bmatrix} \mathbf{u}_{1,2} \\ \vdots \\ \mathbf{u}_{N-1,2} \\ \mathbf{g}_3 \end{bmatrix} A_{(N+1:2N)}^\top. \quad (13)$$

By (11), the coefficient matrix on the RHS of (13) is full-rank. Therefore, both users can decode $A_{(N+1:2N)}$ as

$$A_{(N+1:2N)}^\top = \begin{bmatrix} \mathbf{u}_{1,2} \\ \vdots \\ \mathbf{u}_{N-1,2} \\ \mathbf{g}_3 \end{bmatrix}^{-1} \begin{bmatrix} A_{1,2}^{[(1,2)]} - A_{N,4}^{[(1,2)]} \\ \vdots \\ A_{N-1,2}^{[(1,2)]} - A_{N,4}^{[(1,2)]} \\ A_{N,3}^{[(1,2)]} \end{bmatrix}. \quad (14)$$

Similarly, due to the alignment condition of (12a), we have $A_{N,1}^{[(1,2)]} = \mathbf{g}_1 A_{(1:N)}^\top = \mathbf{u}_{n,1} A_{(1:N)}^\top, \forall n \in [N-1]$, i.e., $A_{(1:N)}$ are aligned among $A_{1,1}^{[(1,2)]}, \dots, A_{N-1,1}^{[(1,2)]}$. Subtracting $A_{N,1}^{[(1,2)]}$ from $A_{1,1}^{[(1,2)]}, \dots, A_{N-1,1}^{[(1,2)]}$, and by the full-rank condition (11), $B_{(1:N)}$ can be decoded by both users as

$$B_{(1:N)}^\top = \begin{bmatrix} \mathbf{v}_{1,1} \\ \vdots \\ \mathbf{v}_{N-1,1} \\ \mathbf{g}_2 \end{bmatrix}^{-1} \begin{bmatrix} A_{1,1}^{[(1,2)]} - A_{N,1}^{[(1,2)]} \\ \vdots \\ A_{N-1,1}^{[(1,2)]} - A_{N,1}^{[(1,2)]} \\ A_{N,2}^{[(1,2)]} \end{bmatrix}. \quad (15)$$

Now the message bits $A_{(N+1:2N)}, B_{(1:N)}$ are available to both users. User 1 still needs $A_{(1:N)}$ and user 2 still needs $B_{(N+1:2N)}$. Removing the interference of $B_{(1:N)}$ from Z_1 , user 1 obtains $N-1$ linear combinations of $A_{(1:N)}$. Together with $A_{N,1}^{[(1,2)]} = \mathbf{g}_1 A_{(1:N)}^\top$, user 1 obtains N independent linear combinations of $A_{(1:N)}$, from which $A_{(1:N)}$ can be decoded as

$$A_{(1:N)}^\top = \begin{bmatrix} \alpha_{1,1} \\ \vdots \\ \alpha_{1,N-1} \\ \mathbf{g}_1 \end{bmatrix}^{-1} \begin{bmatrix} Z_{1,1} - \beta_{1,1} B_{(1:N)}^\top \\ \vdots \\ Z_{1,N-1} - \beta_{1,N-1} B_{(1:N)}^\top \\ A_{N,1}^{[(1,2)]} \end{bmatrix}. \quad (16)$$

As a result, user 1 correctly decodes all the $2N$ bits of the desired message A . Similarly, user 2 can also correctly decode all the $2N$ bits of message B .

For $(\theta_1, \theta_2) = (1, 1)$, the following six coefficient matrices

$$\begin{bmatrix} \alpha_{1,1} \\ \alpha_{1,2} \\ \vdots \\ \alpha_{1,N-1} \\ \mathbf{g}_1 \end{bmatrix}, \begin{bmatrix} \alpha_{2,1} \\ \alpha_{2,2} \\ \vdots \\ \alpha_{2,N-1} \\ \mathbf{g}_3 \end{bmatrix}, \begin{bmatrix} \beta_{1,1} \\ \beta_{1,2} \\ \vdots \\ \beta_{1,N-1} \\ \mathbf{g}_2 \end{bmatrix}, \begin{bmatrix} \beta_{2,1} \\ \beta_{2,2} \\ \vdots \\ \beta_{2,N-1} \\ \mathbf{g}_4 \end{bmatrix},$$

$$\times \begin{bmatrix} \mathbf{u}_{1,1} \\ \mathbf{u}_{2,1} \\ \vdots \\ \mathbf{u}_{N-1,1} \\ \mathbf{g}_1 \end{bmatrix}, \begin{bmatrix} \mathbf{u}_{1,2} \\ \mathbf{u}_{2,2} \\ \vdots \\ \mathbf{u}_{N-1,2} \\ \mathbf{g}_3 \end{bmatrix} \quad (17)$$

are required to be full-rank. The alignment condition is

$$\mathbf{g}_2 = \mathbf{v}_{n,1}, \quad \forall n \in [N-1], \quad (18a)$$

$$\mathbf{g}_4 = \mathbf{v}_{n,2}, \quad \forall n \in [N-1]. \quad (18b)$$

With the above conditions, we next show that both users can correctly recover message A .

Due to (18a), we have $A_{N,2}^{[(1,1)]} = \mathbf{g}_2 B_{(1:N)}^\top = \mathbf{v}_{n,1} B_{(1:N)}^\top, \forall n \in [N-1]$. Subtracting $A_{N,2}^{[(1,1)]}$ from $A_{n,1}^{[(1,1)]}, \dots, A_{N-1,1}^{[(1,1)]}$, and by the full-rank condition (17), both users can decode $A_{(1:N)}^{[(1,1)]}$ as

$$A_{(1:N)}^\top = \begin{bmatrix} \mathbf{u}_{1,1} \\ \mathbf{u}_{2,1} \\ \vdots \\ \mathbf{u}_{N-1,1} \\ \mathbf{g}_1 \end{bmatrix}^{-1} \begin{bmatrix} A_{1,1}^{[(1,1)]} - A_{N,2}^{[(1,1)]} \\ A_{2,1}^{[(1,1)]} - A_{N,2}^{[(1,1)]} \\ \vdots \\ A_{N-1,1}^{[(1,1)]} - A_{N,2}^{[(1,1)]} \\ A_{N,1}^{[(1,1)]} \end{bmatrix}. \quad (19)$$

Also, due to (18b), we have $A_{N,4}^{[(1,1)]} = \mathbf{g}_4 B_{(N+1:2N)}^\top = \mathbf{v}_{n,2} B_{(N+1:2N)}^\top, \forall n \in [N-1]$. Subtracting $A_{N,4}^{[(1,1)]}$ from $A_{n,2}^{[(1,1)]}, \dots, A_{N-1,2}^{[(1,1)]}$, and by (17), both users can decode $A_{(N+1:2N)}^{[(1,1)]}$ as

$$A_{(N+1:2N)}^\top = \begin{bmatrix} \mathbf{u}_{1,2} \\ \vdots \\ \mathbf{u}_{N-1,2} \\ \mathbf{g}_3 \end{bmatrix}^{-1} \begin{bmatrix} A_{1,2}^{[(1,1)]} - A_{N,4}^{[(1,1)]} \\ \vdots \\ A_{N-1,2}^{[(1,1)]} - A_{N,4}^{[(1,1)]} \\ A_{N,3}^{[(1,1)]} \end{bmatrix}. \quad (20)$$

As a result, both users correctly recover message A .

Remark 2: One interesting observation is that, for the case of identical demands, i.e., $(\theta_1, \theta_2) = (1, 1)$ or $(2, 2)$, the cached contents of the users are actually not used in the decoding process. This means that caching does not help to improve the PIR load in the case of identical demands for the considered problem setup $K = K_u = 2$.

With the above full-rank and alignment conditions, we now employ a randomized specification of the linear combination coefficients used by each DB and formally describe the delivery scheme.

We first introduce some necessary notations. Define a binary matrix $\mathbf{Y}_N \in \mathbb{F}_2^{N \times N}$ ($N \geq 2$) as

$$\mathbf{Y}_N \triangleq \begin{bmatrix} \mathbf{I}_{N-1} & \mathbf{1}_{N-1}^\top \\ \mathbf{0}_{N-1} & 1 \end{bmatrix} \quad (21)$$

It can be seen that $\text{rank}(\mathbf{Y}_N) = N$. Let $\mathcal{Y}_N \triangleq \{\mathbf{Y}_N(i, :) : i \in [N]\}$ be a set that contains the rows of \mathbf{Y}_N . Also define two binary matrices $\mathbf{M}(\mathbf{u}_{:,i}, \mathbf{g}_j), \mathbf{M}(\mathbf{v}_{:,i}, \mathbf{g}_j) \in \mathbb{F}_2^{N \times N}$, $\forall i \in [2], \forall j \in [4]$ as

$$\mathbf{M}(\mathbf{u}_{:,i}, \mathbf{g}_j) \triangleq [\mathbf{u}_{1,i}; \mathbf{u}_{2,i}; \dots; \mathbf{u}_{N-1,i}; \mathbf{g}_j], \quad (22a)$$

$$\mathbf{M}(\mathbf{v}_{:,i}, \mathbf{g}_j) \triangleq [\mathbf{v}_{1,i}; \mathbf{v}_{2,i}; \dots; \mathbf{v}_{N-1,i}; \mathbf{g}_j]. \quad (22b)$$

Note that $\mathbf{M}(\mathbf{u}_{:,i}, \mathbf{g}_j)$ and $\mathbf{M}(\mathbf{v}_{:,i}, \mathbf{g}_j)$ represent the coefficient sub-matrices of (10) corresponding to the message bits $A_{(i-1)N+1:iN}$ and $B_{(i-1)N+1:iN}$ respectively.

The delivery strategies for different user demands are given as follows:

- $(\theta_1, \theta_2) = (1, 2)$: Let \mathbf{g}_1 and \mathbf{g}_4 be chosen randomly and uniformly i.i.d. from \mathcal{Y}_N . Also let $\mathbf{M}(\mathbf{u}_{:,2}, \mathbf{g}_3)$ and $\mathbf{M}(\mathbf{v}_{:,1}, \mathbf{g}_2)$ be two independent random permutations

of the rows of \mathbf{Y}_N . It can be seen that with such a specification of the answer coefficients and the previously defined cache coefficients, the full-rank and the alignment conditions (11), (12) are satisfied. Therefore, both users can recover their desired messages.

- $(\theta_1, \theta_2) = (1, 1)$: Let \mathbf{g}_2 and \mathbf{g}_4 be chosen randomly and uniformly i.i.d. from \mathcal{Y}_N . Also let $\mathbf{M}(\mathbf{u}_{:,1}, \mathbf{g}_1)$ and $\mathbf{M}(\mathbf{u}_{:,2}, \mathbf{g}_3)$ be two independent random permutations of the rows of \mathbf{Y}_N . It can be seen that the full-rank and alignment conditions (17), (18) are satisfied.
- $(\theta_1, \theta_2) = (2, 1)$: Let \mathbf{g}_2 and \mathbf{g}_3 be chosen randomly and uniformly i.i.d. from \mathcal{Y}_N . Also let $\mathbf{M}(\mathbf{u}_{:,1}, \mathbf{g}_1)$ and $\mathbf{M}(\mathbf{u}_{:,2}, \mathbf{g}_4)$ be two independent random permutations of the rows of \mathbf{Y}_N . It can be verified that the corresponding full-rank and alignment conditions are satisfied.
- $(\theta_1, \theta_2) = (2, 2)$: Let \mathbf{g}_1 and \mathbf{g}_3 be chosen randomly and uniformly i.i.d. from \mathcal{Y}_N . Also let $\mathbf{M}(\mathbf{v}_{:,1}, \mathbf{g}_2)$ and $\mathbf{M}(\mathbf{v}_{:,2}, \mathbf{g}_4)$ be two independent random permutations of the rows of \mathbf{Y}_N .

We next prove the correctness and privacy of the above delivery scheme.

Correctness: Since the random specification of the answer coefficients satisfies the corresponding full-rank and alignment conditions for any $(\theta_1, \theta_2) \in [2]^2$, decodability is guaranteed.

Privacy: WLOG, we prove that the above delivery scheme is private from DB 1's viewpoint, i.e., the demand vector θ is equally likely to be $(1, 2), (2, 1), (1, 1)$ or $(2, 2)$. More specifically, let $\mathbf{x} \triangleq [\mathbf{u}_{1,1}, \mathbf{u}_{1,2}, \mathbf{v}_{1,1}, \mathbf{v}_{1,2}] \in \mathcal{Y}_N^{1 \times 4}$ be a random realization of the answer linear coefficients of DB 1. Let $\Gamma(\mathbf{u}_{1,j}, \theta)$ denote a random query of the value of $\mathbf{u}_{1,j}, j = 1, 2$ to DB 1 when the demand vector is θ . Other notations follow similarly. Let $\mathbf{X}(\theta) \triangleq [\Gamma(\mathbf{u}_{1,1}, \theta), \Gamma(\mathbf{u}_{1,2}, \theta), \Gamma(\mathbf{v}_{1,1}, \theta), \Gamma(\mathbf{v}_{1,2}, \theta)]$ represent the random query to DB 1 when the user demand vector is θ . Then the probability that \mathbf{x} is generated for θ , i.e., $\mathbf{X}(\theta) = \mathbf{x}$, is equal to

$$\begin{aligned} P(\mathbf{X}(\theta) = \mathbf{x}) &= P(\Gamma(\mathbf{u}_{1,1}, \theta) = \mathbf{u}_{1,1}) \times P(\Gamma(\mathbf{u}_{1,2}, \theta) = \mathbf{u}_{1,2}) \\ &\quad \times P(\Gamma(\mathbf{v}_{1,1}, \theta) = \mathbf{v}_{1,1}) \times P(\Gamma(\mathbf{v}_{1,2}, \theta) = \mathbf{v}_{1,2}) \quad (23a) \end{aligned}$$

$$= \begin{cases} \frac{1}{N} \times \frac{(N-1)!}{N!} \times \frac{(N-1)!}{N!} \times \frac{1}{N} = \frac{1}{N^4}, & \text{if } \theta = (1, 2) \\ \frac{(N-1)!}{N!} \times \frac{1}{N} \times \frac{1}{N} \times \frac{(N-1)!}{N!} = \frac{1}{N^4}, & \text{if } \theta = (2, 1) \\ \frac{(N-1)!}{N!} \times \frac{(N-1)!}{N!} \times \frac{1}{N} \times \frac{1}{N} = \frac{1}{N^4}, & \text{if } \theta = (1, 1) \\ \frac{1}{N} \times \frac{1}{N} \times \frac{(N-1)!}{N!} \times \frac{(N-1)!}{N!} = \frac{1}{N^4}, & \text{if } \theta = (2, 2) \end{cases} \quad (23b)$$

in which (23a) is because the specifications of the query vectors $\Gamma(\mathbf{u}_{1,1}, \boldsymbol{\theta})$, $\Gamma(\mathbf{u}_{1,2}, \boldsymbol{\theta})$, $\Gamma(\mathbf{v}_{1,1}, \boldsymbol{\theta})$ and $\Gamma(\mathbf{v}_{1,2}, \boldsymbol{\theta})$ are independent of each other according to the delivery design. Moreover, for $\boldsymbol{\theta} = (1, 2)$, because $\mathbf{u}_{1,1}$ and $\mathbf{v}_{1,2}$ are chosen i.i.d. from \mathcal{Y}_N , we have $P(\Gamma(\mathbf{u}_{1,1}, \boldsymbol{\theta}) = \mathbf{u}_{1,1}) = P(\Gamma(\mathbf{v}_{1,2}, \boldsymbol{\theta}) = \mathbf{v}_{1,2}) = \frac{1}{N}$. Also, because $\mathbf{M}(\mathbf{u}_{1,2}, \mathbf{g}_3)$ and $\mathbf{M}(\mathbf{v}_{1,1}, \mathbf{g}_2)$ are two independent random permutations of \mathbf{Y}_N , we have $P(\Gamma(\mathbf{u}_{1,2}, \boldsymbol{\theta}) = \mathbf{u}_{1,2}) = P(\Gamma(\mathbf{v}_{1,1}, \boldsymbol{\theta}) = \mathbf{v}_{1,1}) = \frac{(N-1)!}{N!} = \frac{1}{N}$. Therefore, we have $P(\mathbf{X}(\boldsymbol{\theta}) = \mathbf{x}) = \frac{1}{N^4}$ if $\boldsymbol{\theta} = (1, 2)$. The probabilities for other demands can be calculated similarly. Since $P(\mathbf{X}(\boldsymbol{\theta}) = \mathbf{x})$ does not depend on $\boldsymbol{\theta}$, from DB 1's viewpoint, the coefficient realization \mathbf{x} is equally likely to be generated for $\boldsymbol{\theta} = (1, 2)$, $(2, 1)$, $(1, 1)$ or $(2, 2)$. Therefore, the scheme is private from DB 1's point of view. Due to symmetry, the scheme is also private from any other DB's viewpoint. As a result, the proposed delivery scheme is private.

Performance: Since $D = 2N + 2$ linear combinations, each containing one bit, are downloaded in total, the achieved load is $R = \frac{N+1}{N}$.

We provide the following example to illustrate the above design.

Example 1: (Achievability of $(1/4, 3/2)$ for $N = 2$) Consider the cache-aided MuPIR problem with $K = K_u = N = 2$. The cache placement and private delivery phases are as follows.

3) *Cache Placement:* Each message consists of $L = 4$ bits, i.e., $A = (A_1, A_2, A_3, A_4)$, $B = (B_1, B_2, B_3, B_4)$. Each user stores a linear combination of the message bits which are $Z_1 = \alpha_{1,1}[A_1, A_2]^T + \beta_{1,1}[B_1, B_2]^T = A_1 + B_1$, $Z_2 = \alpha_{2,1}[A_3, A_4]^T + \beta_{2,1}[B_3, B_4]^T = A_3 + B_3$ where the coefficients are chosen as $\alpha_{1,1} = \alpha_{2,1} = \beta_{1,1} = \beta_{2,1} = [1, 0]$.⁵ Therefore, $M = 1/4$.

4) *Private Delivery:* The answers are

$$\begin{bmatrix} A_{1,1}^{[\boldsymbol{\theta}]} \\ A_{1,2}^{[\boldsymbol{\theta}]} \\ A_{2,1}^{[\boldsymbol{\theta}]} \\ A_{2,2}^{[\boldsymbol{\theta}]} \\ A_{2,3}^{[\boldsymbol{\theta}]} \\ A_{2,4}^{[\boldsymbol{\theta}]} \end{bmatrix} = \begin{bmatrix} \mathbf{u}_{1,1} & \mathbf{0}_2 & \mathbf{v}_{1,1} & \mathbf{0}_2 \\ \mathbf{0}_2 & \mathbf{u}_{1,2} & \mathbf{0}_2 & \mathbf{v}_{1,2} \\ \mathbf{g}_1 & \mathbf{0}_2 & \mathbf{0}_2 & \mathbf{0}_2 \\ \mathbf{0}_2 & \mathbf{0}_2 & \mathbf{g}_2 & \mathbf{0}_2 \\ \mathbf{0}_2 & \mathbf{g}_3 & \mathbf{0}_2 & \mathbf{0}_2 \\ \mathbf{0}_2 & \mathbf{0}_2 & \mathbf{0}_2 & \mathbf{g}_4 \end{bmatrix} \begin{bmatrix} A_{(1:2)}^T \\ A_{(3:4)}^T \\ B_{(1:2)}^T \\ B_{(3:4)}^T \end{bmatrix}. \quad (24)$$

Suppose the demand vector is $(\theta_1, \theta_2) = (1, 2)$. For this demand vector, we let $\mathbf{u}_{1,1} = \mathbf{g}_1$ and $\mathbf{v}_{1,2} = \mathbf{g}_4$ as shown in (12). To specify the coefficients in (24), we introduce the matrix $\mathbf{Y}_2 = \begin{bmatrix} 1 & 1 \\ 0 & 1 \end{bmatrix}$ which is independent of the $\boldsymbol{\theta}$. We let \mathbf{g}_1 and \mathbf{g}_4 be chosen randomly and uniformly i.i.d. from $\mathcal{Y}_2 = \{[1, 1], [0, 1]\}$. In addition, we let $[\mathbf{u}_{1,2}; \mathbf{g}_3]$ and $[\mathbf{v}_{1,1}; \mathbf{g}_2]$ be two independent random permutations of the rows of \mathbf{Y}_2 .

⁵With a slight abuse of notation, here we use Z_1, Z_2 to denote the cached bits by the users despite they are defined as sets.

Correctness: From $A_{1,2}^{[\boldsymbol{\theta}]} - A_{2,4}^{[\boldsymbol{\theta}]}$, each user obtains $\mathbf{u}_{1,2}[A_3, A_4]^T$. In addition, each user receives $A_{2,3}^{[\boldsymbol{\theta}]} = \mathbf{g}_3[A_3, A_4]^T$. Since $\mathbf{u}_{1,2}$ and \mathbf{g}_3 are two different rows of \mathbf{Y}_2 , it can be seen that they are linearly independent. Thus A_3 and A_4 can be decoded by the users. From $A_{1,1}^{[\boldsymbol{\theta}]} - A_{2,1}^{[\boldsymbol{\theta}]}$, each user obtains $\mathbf{v}_{1,1}[B_1, B_2]^T$. Also, each user receives $A_{2,2}^{[\boldsymbol{\theta}]} = \mathbf{g}_2[B_1, B_2]^T$. Since $\mathbf{v}_{1,1}$ and \mathbf{g}_2 are linearly independent, each user can decode B_1 and B_2 . User 1 caches $A_1 + B_1$ and has decoded B_1 , it can then decode A_1 . User 1 also receives $A_{2,1}^{[\boldsymbol{\theta}]} = \mathbf{g}_1[A_1, A_2]^T$ where $\mathbf{g}_1 \in \mathcal{Y}_2$. Due to the design of \mathcal{Y}_2 , for both choices of \mathbf{g}_1 , user 1 can always decode A_1 and A_2 from $\mathbf{g}_1[A_1, A_2]^T$. Therefore, user 1 can recover message A . Similarly, since user 2 caches $A_3 + B_3$ and has decoded A_3 , it can then decode B_3 . User 2 also receives $A_{2,4}^{[\boldsymbol{\theta}]} = \mathbf{g}_4[B_3, B_4]^T$ where $\mathbf{g}_4 \in \mathcal{Y}_2$. It can be seen that user 2 can always decode B_3, B_4 regardless of the choice of \mathbf{g}_4 . Therefore, both users can recover their desired messages, proving the correctness of the scheme.

Privacy: From the viewpoint of DB 1, whose sent linear combinations are $\mathbf{g}_1[A_1, A_2]^T + \mathbf{v}_{1,1}[B_1, B_2]^T$ and $\mathbf{u}_{1,2}[A_3, A_4]^T + \mathbf{g}_4[B_3, B_4]^T$, the vectors $\mathbf{g}_1, \mathbf{g}_4, \mathbf{v}_{1,1}, \mathbf{u}_{1,2}$ appear to be chosen randomly and uniformly i.i.d. from \mathcal{Y}_2 regardless of $\boldsymbol{\theta}$. Thus, these linear combinations are independent of $\boldsymbol{\theta}$. From the viewpoint of DB 2, whose sent linear combinations are $\mathbf{g}_1[A_1, A_2]^T, \mathbf{g}_2[B_1, B_2]^T, \mathbf{g}_3[A_3, A_4]^T$, and $\mathbf{g}_4[B_3, B_4]^T$, the vectors $\mathbf{g}_1, \mathbf{g}_2, \mathbf{g}_3, \mathbf{g}_4$ also appear to be chosen randomly and uniformly i.i.d. from \mathcal{Y}_2 , implying the independence between the coefficients and $\boldsymbol{\theta}$. Therefore, each DB cannot get any information about $\boldsymbol{\theta}$ from its answer and the user cache.

Performance: The achieved load is $R = 3/2$. \diamond

B. Achievability of $\left(\frac{2(N-1)}{2N-1}, \frac{N+1}{2N-1}\right)$

The cache placement and private delivery phases are described as follows.

1) *Cache Placement:* Let $W_1 = A, W_2 = B$ be the two messages each of which has $L = 2N - 1$ bits, i.e., $A = (A_1, \dots, A_{2N-1})$, $B = (B_1, \dots, B_{2N-1})$. Each user stores $2(N - 1)$ bits of each message (therefore $M = \frac{2(N-1)}{2N-1}$), i.e.,

$$Z_1 = \{A_{1:N-1}, B_{1:N-1}\}, \quad (25a)$$

$$Z_2 = \{A_{N:2N-2}, B_{N:2N-2}\}. \quad (25b)$$

2) *Private Delivery:* We first construct the answers from the DBs. The answer of DB $n \in [N - 1]$ is a linear combination of certain message bits which is $A_n^{[\boldsymbol{\theta}]} = \mathbf{u}_n A_{(1:2N-1)}^T + \mathbf{v}_n B_{(1:2N-1)}^T$. The answer of DB N consists of two linear combinations, i.e., $A_N^{[\boldsymbol{\theta}]} = (A_{N,1}^{[\boldsymbol{\theta}]}, A_{N,2}^{[\boldsymbol{\theta}]})$ where $A_{N,1}^{[\boldsymbol{\theta}]} = \mathbf{g}_1 A_{(1:2N-1)}^T$ and $A_{N,2}^{[\boldsymbol{\theta}]} = \mathbf{g}_2 B_{(1:2N-1)}^T$. The coefficient vectors $\mathbf{u}_n \triangleq [u_{n,1}, \dots, u_{n,2N-1}]$, $\mathbf{v}_n \triangleq [v_{n,1}, \dots, v_{n,2N-1}]$, $\forall n \in [N - 1]$, $\mathbf{g}_j \triangleq [g_{j,1}, \dots, g_{j,2N-1}]$, $\forall j \in [2]$ belong to $\mathbb{F}_2^{1 \times (2N-1)} \setminus \{\mathbf{0}_{2N-1}\}$ and are subject to design according to the user demands.

These answers can be written as

$$\begin{aligned}
 \begin{bmatrix} A_1^{[\theta]} \\ \vdots \\ A_{N-1}^{[\theta]} \\ A_{N,1}^{[\theta]} \\ A_{N,2}^{[\theta]} \end{bmatrix} &= \begin{bmatrix} \mathbf{u}_1 & \mathbf{v}_1 \\ \vdots & \vdots \\ \mathbf{u}_{N-1} & \mathbf{v}_{N-1} \\ \mathbf{g}_1 & \mathbf{0}_{2N-1} \\ \mathbf{0}_{2N-1} & \mathbf{g}_2 \end{bmatrix} \begin{bmatrix} A_{(1:2N-1)}^\top \\ B_{(1:2N-1)}^\top \end{bmatrix} \\
 &= \begin{bmatrix} \mathbf{u}_{1,(1:2N-2)} & 1 & \mathbf{v}_{1,(1:2N-2)} & 1 \\ \vdots & \vdots & \vdots & \vdots \\ \mathbf{u}_{N-1,(1:2N-2)} & 1 & \mathbf{v}_{N-1,(1:2N-2)} & 1 \\ \mathbf{g}_{1,(1:2N-2)} & 1 & \mathbf{0}_{2N-2} & 0 \\ \mathbf{0}_{2N-2} & 0 & \mathbf{g}_{2,(1:2N-2)} & 1 \end{bmatrix} \\
 &\quad \times \begin{bmatrix} A_{(1:2N-1)}^\top \\ B_{(1:2N-1)}^\top \end{bmatrix}, \quad (26)
 \end{aligned}$$

where we fix the coefficients $u_{n,2N-1} = v_{n,2N-1} = 1$, $\forall n \in [N-1]$ and $g_{1,2N-1} = g_{2,2N-1} = 1$.

We next consider different demands and present the necessary full-rank and alignment conditions. Due to space limit, we focus on the cases of $(\theta_1, \theta_2) = (1, 2)$ and $(1, 1)$.

For $(\theta_1, \theta_2) = (1, 2)$, the following two coefficient matrices are required to be full-rank:

Full-rank condition: the following matrices are full-rank,

$$\begin{bmatrix} \mathbf{u}_{1,(N:2N-1)} \\ \vdots \\ \mathbf{u}_{N-1,(N:2N-1)} \\ \mathbf{g}_{1,(N:2N-1)} \end{bmatrix}, \begin{bmatrix} [\mathbf{v}_{1,(1:N-1)}, v_{1,2N-1}] \\ \vdots \\ [\mathbf{v}_{N-1,(1:N-1)}, v_{N-1,2N-1}] \\ [\mathbf{g}_{2,(1:N-1)}, g_{2,2N-1}] \end{bmatrix}, \quad (27)$$

where $\mathbf{u}_{1,(N:2N-1)} \triangleq [u_{1,N}, u_{1,N+1}, \dots, u_{1,2N-1}]$ and other notation follows similarly. For alignment, we let $\forall n \in [N-1]$:

Alignment condition:

$$[\mathbf{u}_{n,(1:N-1)}, u_{n,2N-1}] = [\mathbf{g}_{1,(1:N-1)}, g_{1,2N-1}], \quad (28a)$$

$$\mathbf{v}_{n,(N:2N-1)} = \mathbf{g}_{2,(N:2N-1)}, \quad (28b)$$

i.e., the message bits $A_{(1:N-1)}, A_{2N-1}$ are aligned among the linear combinations $A_n^{[(1,2)]}, \forall n \in [N-1]$ and $A_{N,1}^{[(1,2)]}$; the bits $B_{(N:2N-1)}$ are aligned among $A_n^{[(1,2)]}, \forall n \in [N-1]$ and $A_{N,2}^{[(1,2)]}$. We next show that the users can recover their desired messages with the above conditions.

For user 1, due to the alignment of $B_{(N:2N-1)}$, we have

$$\begin{aligned}
 A_{N,2}^{[(1,2)]} - \mathbf{g}_{2,(1:N-1)} B_{(1:N-1)}^\top &= \mathbf{g}_{2,(N:2N-1)} B_{(N:2N-1)}^\top \\
 &= \mathbf{v}_{n,(N:2N-1)} B_{(N:2N-1)}^\top, \\
 &\quad \times \forall n \in [N-1]. \quad (29)
 \end{aligned}$$

Subtracting $A_{N,2}^{[(1,2)]} - \mathbf{g}_{2,(1:N-1)} B_{(1:N-1)}^\top$ (this is known to user 1 since $B_{1:N-1}$ are already cached by user 1) from $A_1^{[(1,2)]}, \dots, A_{N-1}^{[(1,2)]}$ in (26), together with $A_{N,1}^{[(1,2)]} = \mathbf{g}_{1,(1:2N-1)}$, user 1 obtains N independent linear combinations of $A_{(N:2N-1)}$, which can be decoded as

$$A_{(N:2N-1)}^\top = \begin{bmatrix} \mathbf{u}_{1,(N:2N-1)} \\ \vdots \\ \mathbf{u}_{N-1,(N:2N-1)} \\ \mathbf{g}_{1,(N:2N-1)} \end{bmatrix}^{-1} \mathbf{y}, \quad (30)$$

where \mathbf{y} is defined as

$$\begin{aligned}
 \mathbf{y} \triangleq & \begin{bmatrix} A_1^{[(1,2)]} - (A_{N,2}^{[(1,2)]} - \mathbf{g}_{2,(1:N-1)} B_{(1:N-1)}^\top) \\ \vdots \\ A_{N-1}^{[(1,2)]} - (A_{N,2}^{[(1,2)]} - \mathbf{g}_{2,(1:N-1)} B_{(1:N-1)}^\top) \\ A_{N,1}^{[(1,2)]} \end{bmatrix} \\
 & - \begin{bmatrix} \mathbf{u}_{1,(1:N-1)} & \mathbf{v}_{1,(1:N-1)} \\ \vdots & \vdots \\ \mathbf{u}_{N-1,(1:N-1)} & \mathbf{v}_{N-1,(1:N-1)} \\ \mathbf{g}_{1,(1:N-1)} & \mathbf{0}_{N-1} \end{bmatrix} \begin{bmatrix} A_{(1:N-1)}^\top \\ B_{(1:N-1)}^\top \end{bmatrix}. \quad (31)
 \end{aligned}$$

Since the message bits $A_{(1:N-1)}, B_{(1:N-1)}$ are cached by user 1, it can decode the bits $A_{(N:2N-1)}$ and then recover message A . Similarly, user 2 can correctly recover message B .

For $(\theta_1, \theta_2) = (1, 1)$, the following two coefficient matrices should be full-rank:

$$\begin{bmatrix} \mathbf{u}_{1,(N:2N-1)} \\ \mathbf{u}_{2,(N:2N-1)} \\ \vdots \\ \mathbf{u}_{N-1,(N:2N-1)} \\ \mathbf{g}_{1,(N:2N-1)} \end{bmatrix}, \begin{bmatrix} [\mathbf{u}_{1,(1:N-1)}, u_{1,2N-1}] \\ [\mathbf{u}_{2,(1:N-1)}, u_{2,2N-1}] \\ \vdots \\ [\mathbf{u}_{N-1,(1:N-1)}, u_{N-1,2N-1}] \\ [\mathbf{g}_{1,(1:N-1)}, g_{1,2N-1}] \end{bmatrix}. \quad (32)$$

The alignment condition is

$$\mathbf{g}_2 = \mathbf{v}_n, \quad \forall n \in [N-1]. \quad (33)$$

The decoding process is explained as follows. Due to the alignment of (33), we have

$$A_{N,2}^{[(1,1)]} = \mathbf{g}_2 B_{(1:2N-1)}^\top = \mathbf{v}_n B_{(1:2N-1)}^\top, \quad \forall n \in [N-1]. \quad (34)$$

Subtracting $A_{N,2}^{[(1,1)]}$ from all $A_n^{[(1,1)]}, \forall n \in [N-1]$, we obtain

$$A_{(N:2N-1)}^\top = \begin{bmatrix} \mathbf{u}_{1,(N:2N-1)} \\ \mathbf{u}_{2,(N:2N-1)} \\ \vdots \\ \mathbf{u}_{N-1,(N:2N-1)} \\ \mathbf{g}_{1,(N:2N-1)} \end{bmatrix}^{-1} \times \left(\begin{bmatrix} A_1^{[(1,1)]} - A_{N,2}^{[(1,1)]} \\ A_2^{[(1,1)]} - A_{N,2}^{[(1,1)]} \\ \vdots \\ A_{N-1}^{[(1,1)]} - A_{N,2}^{[(1,1)]} \\ A_{N,1}^{[(1,1)]} \end{bmatrix} - \begin{bmatrix} \mathbf{u}_{1,(1:N-1)} \\ \mathbf{u}_{2,(1:N-1)} \\ \vdots \\ \mathbf{u}_{N-1,(1:N-1)} \\ \mathbf{g}_{1,(1:N-1)} \end{bmatrix} A_{(1:N-1)}^\top \right). \quad (35)$$

Since the bits $A_{1:N-1}$ are cached by user 1, it can decode the desired bits $A_{N:2N-1}$ and then recover A . Similarly, user 2 can decode the desired bits $A_{1:N-1}, A_{2N-1}$. Therefore, both users can correctly recover message A .

With the above full-rank and alignment conditions, we now employ a randomized specification of the linear coefficients used by each DB and formally describe the delivery scheme.

We first introduce some necessary notation. Let $\mathbf{Y}'_N \triangleq [\mathbf{I}_{N-1}; \mathbf{0}_{N-1}] \in \mathbb{F}_2^{N \times (N-1)}$ and let $\mathcal{Y}'_N \triangleq \{\mathbf{Y}'_N(i, :): i \in [N]\}$ be a set containing the rows of \mathbf{Y}'_N . For an index vector $(m : n) = (m, m+1, \dots, n)$, define $\forall j \in [2]$:

$$\mathbf{M}'(\mathbf{u}, \mathbf{g}_j, (m : n)) \triangleq [\mathbf{u}_{1,(m:n)}; \dots; \mathbf{u}_{N-1,(m:n)}; \mathbf{g}_{j,(m:n)}] \in \mathbb{F}_2^{N \times (n-m+1)}, \quad (36a)$$

$$\mathbf{M}'(\mathbf{v}, \mathbf{g}_j, (m : n)) \triangleq [\mathbf{v}_{1,(m:n)}; \dots; \mathbf{v}_{N-1,(m:n)}; \mathbf{g}_{j,(m:n)}] \in \mathbb{F}_2^{N \times (n-m+1)}. \quad (36b)$$

It can be seen that $\mathbf{M}'(\mathbf{u}, \mathbf{g}_j, (m : n))$ and $\mathbf{M}'(\mathbf{v}, \mathbf{g}_j, (m : n))$ represent the coefficient sub-matrices of (26) corresponding to the message bits $A_{(m:n)}$ and $B_{(m:n)}$ respectively.

The delivery strategies for different demands are as follows.

- $(\theta_1, \theta_2) = (1, 2)$: Let $\mathbf{g}_{1,(1:N-1)}$ and $\mathbf{g}_{2,(N:2N-2)}$ be chosen randomly and uniformly i.i.d. from \mathcal{Y}'_N . Also, let $\mathbf{M}'(\mathbf{u}, \mathbf{g}_1, (N : 2N-2))$ and $\mathbf{M}'(\mathbf{v}, \mathbf{g}_2, (1 : N-1))$ be two independent random permutations of the rows of \mathbf{Y}'_N . It can be easily seen that the full-rank condition of (27) is satisfied, guaranteeing the decodability.
- $(\theta_1, \theta_2) = (1, 1)$: Let $\mathbf{g}_{2,(N:2N-2)}$ and $\mathbf{g}_{2,(1:N-1)}$ be chosen randomly and uniformly i.i.d. from \mathcal{Y}'_N . Also, let $\mathbf{M}'(\mathbf{u}, \mathbf{g}_1, (N : 2N-2))$ and $\mathbf{M}'(\mathbf{u}, \mathbf{g}_1, (1 : N-1))$ be chosen as two independent random permutations of the rows of \mathbf{Y}'_N .
- $(\theta_1, \theta_2) = (2, 1)$: Let $\mathbf{g}_{1,(N:2N-2)}$ and $\mathbf{g}_{2,(1:N-1)}$ be chosen randomly and uniformly i.i.d. from \mathcal{Y}'_N . Also, let $\mathbf{M}'(\mathbf{u}, \mathbf{g}_1, (1 : N-1))$ and $\mathbf{M}'(\mathbf{v}, \mathbf{g}_2, (N : 2N-2))$ be chosen as two independent random permutations of the rows of \mathbf{Y}'_N .

- $(\theta_1, \theta_2) = (2, 2)$: Let $\mathbf{g}_{1,(N:2N-2)}$ and $\mathbf{g}_{1,(1:N-1)}$ be chosen randomly and uniformly i.i.d. from \mathcal{Y}'_N . Also, let $\mathbf{M}'(\mathbf{v}, \mathbf{g}_2, (1 : N-1))$ and $\mathbf{M}'(\mathbf{v}, \mathbf{g}_2, (N : 2N-2))$ be chosen as two independent random permutations of the rows of \mathbf{Y}'_N .

Correctness: Decodability is straightforward since the randomized specifications of the linear coefficients guarantee the corresponding full-rank and alignment conditions.

Privacy: The privacy can be proved by a similar argument to that of (23).

Performance: Since $D = N + 1$ linear combinations, each containing one bit, are downloaded in total, the achieved load is $R = \frac{N+1}{2N-1}$.

The following example is provided to illustrate the above design idea.

Example 2: (Achievability of $(2/3, 1)$ for $N = 2$) Consider the same setting as Example 1 where $K = K_u = N = 2$. We show the achievability of the memory-load pair $(2/3, 1)$ which is achieved by uncoded placement.

3) **Cache Placement:** Each message consists of $L = 3$ bits, i.e., $A = (A_1, A_2, A_3)$, $B = (B_1, B_2, B_3)$. The cache placement is $Z_1 = \{A_1, B_1\}$, $Z_2 = \{A_2, B_2\}$. Therefore, $M = 2/3$.

4) **Private Delivery:** The answers are written as

$$\begin{bmatrix} A_1^{[\theta]} \\ A_{2,1}^{[\theta]} \\ A_{2,2}^{[\theta]} \end{bmatrix} = \begin{bmatrix} \mathbf{u}_1 & \mathbf{v}_1 \\ \mathbf{g}_1 & \mathbf{0}_3 \\ \mathbf{0}_3 & \mathbf{g}_2 \end{bmatrix} \begin{bmatrix} A_{(1:3)}^\top \\ B_{(1:3)}^\top \end{bmatrix} = \begin{bmatrix} u_{1,1} & u_{1,2} & 1 & v_{1,1} & v_{1,2} & 1 \\ g_{1,1} & g_{1,2} & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & g_{2,1} & g_{2,2} & 1 \end{bmatrix} \begin{bmatrix} A_{(1:3)}^\top \\ B_{(1:3)}^\top \end{bmatrix}. \quad (37)$$

Suppose $(\theta_1, \theta_2) = (1, 2)$. For this demand vector, we let $u_{1,1} = g_{1,1}$, $v_{1,2} = g_{2,2}$ as shown in (28). Thus (37) becomes

$$\begin{bmatrix} A_1^{[\theta]} \\ A_{2,1}^{[\theta]} \\ A_{2,2}^{[\theta]} \end{bmatrix} = \begin{bmatrix} g_{1,1} & u_{1,2} & 1 & v_{1,1} & g_{2,2} & 1 \\ g_{1,1} & g_{1,2} & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & g_{2,1} & g_{2,2} & 1 \end{bmatrix} \begin{bmatrix} A_{(1:3)}^\top \\ B_{(1:3)}^\top \end{bmatrix}. \quad (38)$$

To specify the coefficients, we let $g_{1,1}$ and $g_{2,2}$ be chosen randomly and uniformly i.i.d. from $\{0, 1\}$. Also, let $[u_{1,2}, g_{1,2}]^\top$ and $[v_{1,1}, g_{2,1}]^\top$ be two independent random permutations of $[1, 0]^\top$.

Correctness: We first consider user 1 who stores A_1, B_1 and wants message A . From $A_{2,1}^{[\theta]}$ and A_1 , user 1 obtains $g_{1,2}A_2 + A_3$. From $A_1^{[\theta]} - A_{2,2}^{[\theta]}$, user 1 decodes $g_{1,1}A_1 + u_{1,2}A_2 + A_3 + (v_{1,1} - g_{2,1})B_1$, and then obtains $u_{1,2}A_2 + A_3$ by removing the cached bits A_1, B_1 . Since $u_{1,2} \neq g_{1,2}$, user 1 can solve A_2, A_3 from the two independent linearly combinations $g_{1,2}A_2 + A_3$ and $u_{1,2}A_2 + A_3$. Therefore, user 1 recovers message A . We then consider user 2 who stores A_2, B_2 and wants message B . From $A_{2,2}^{[\theta]}$ and B_2 , user 2 obtains $g_{2,1}B_1 + B_3$. From $A_1^{[\theta]} - A_{2,1}^{[\theta]}$ and the cached bits A_2, B_2 , user 2 obtains $v_{1,1}B_1 + B_3$. Since $v_{1,1} \neq g_{2,1}$, the two linear combinations $g_{2,1}B_1 + B_3$ and $v_{1,1}B_1 + B_3$ are independent, from which B_1, B_3 can be solved by user 2. Therefore, both users can recover their desired messages.

Privacy: From the viewpoint of DB 1 whose sent linear combination is $g_{1,1}A_1 + u_{1,2}A_2 + A_3 + v_{1,1}B_1 + g_{2,2}B_2 + B_3$, the coefficients $g_{1,1}, u_{1,2}, v_{1,1}, g_{2,2}$ appear to be chosen randomly and uniformly i.i.d. from $\{0, 1\}$ regardless of θ . Thus the answer of DB 1 is independent of θ . From the viewpoint of DB 2 whose sent linear combinations are $g_{1,1}A_1 + g_{1,2}A_2 + A_3$ and $g_{2,1}B_1 + g_{2,2}B_2 + B_3$, the coefficients $g_{1,1}, g_{1,2}, g_{2,1}, g_{2,2}$ also appear to be chosen randomly and uniformly i.i.d. from $\{0, 1\}$, implying the independence between the DB 2 answer and θ . Therefore, the delivery scheme is private from both users' viewpoint.

Performance: The achieved load is $R = 1$ for $M = 2/3$. \diamond

V. PROOF OF THEOREM 2: THE PRODUCT DESIGN

In this section, we present a general achievable scheme for arbitrary K, K_u and N , which we call *Product Design* (PD). PD is inspired by both MAN coded caching [6] and the SJ PIR scheme [5] and enjoys combined coding gain.⁶ By comparing with the already established converse bounds for coded caching [33], we show that the PD is optimal within a factor 8 in general as implied by Theorem 2. We first provide an example to highlight the design idea of PD and then present the general achievable schemes.

A. An Example

Example 3: Consider the cache-aided MuPIR problem with $K = 3$ messages, $K_u = 2$ users and $N = 2$ DBs. Let A, B and C denote the three messages. By Theorem 2, the pair $(1, 7/8)$ is achievable.

1) *Cache Placement:* Each message is split into two packets, i.e., $A = (A_1, A_2)$ where A_i represents the i -th packet which consists of 8 bits. Therefore, $L = 16$ bits. Similarly, $B = (B_1, B_2)$, $C = (C_1, C_2)$. User $u \in [2]$ then stores one packet of each message, i.e., the cache is $Z_u = \{A_u, B_u, C_u\}$, satisfying the memory constraint $M = 3/2$.

2) *Private Delivery:* Let $[A_i^1, \dots, A_i^8]$, $[B_i^1, \dots, B_i^8]$ and $[C_i^1, \dots, C_i^8]$ represent three independent random permutations of the 8 bits of the packet A_i, B_i and $C_i, \forall i \in [2]$ respectively. These permutations are known to the users but not to the DBs. Suppose $\theta = (1, 2)$. Let $A_n^{[\theta_1]}(A_2, B_2, C_2)$ represent the answer of DB $n \in [2]$ in the SJ PIR scheme where the corresponding message library is (First messages, second message, third messages) $= (A_2, B_2, C_2)$ and the user demand is θ_1 (i.e., the user demands A_2). Other notations follow similarly. Then the answer of DB n is constructed as $A_n^{[\theta]} = A_n^{[\theta_1]}(A_2, B_2, C_2) + A_n^{[\theta_2]}(A_1, B_1, C_1), \forall n \in [2]$, which is shown in Table I. For other demands, the delivery phase proceeds similarly. We next prove that the above delivery scheme is both correct and private.

Correctness: For $\theta = (1, 2)$, user 1 needs A_2 since A_1 is already in the cache. Similarly, user 2 needs B_1 . For user 1, since A_1, B_1 and C_1 are stored in Z_1 , the interference of $A_n^{[\theta_2]}(A_1, B_1, C_1)$ can be eliminated from $A_n^{[\theta]}, \forall n \in [2]$. Then

⁶Note that, any capacity-achieving PIR scheme for the original single-user non-colluding database PIR problem can be used to combine with the linear caching code to produce a corresponding product design.

TABLE I
ANSWER OF EXAMPLE 3

DB 1 ($A_1^{[\theta]}$)	DB 2 ($A_2^{[\theta]}$)
$A_2^1 + A_1^1$	$A_2^2 + A_1^2$
$B_2^1 + B_1^1$	$B_2^2 + B_1^2$
$C_2^1 + C_1^1$	$C_2^2 + C_1^2$
$A_2^3 + B_2^3 + A_1^3 + B_1^3$	$A_2^5 + B_2^5 + A_1^5 + B_1^5$
$A_2^4 + C_2^4 + A_1^4 + C_1^4$	$A_2^6 + C_2^6 + A_1^6 + C_1^6$
$B_2^3 + C_2^3 + B_1^3 + C_1^3$	$B_2^4 + C_2^4 + B_1^4 + C_1^4$
$A_2^7 + B_2^7 + C_2^7 + A_1^7 + B_1^7 + C_1^7$	$A_2^8 + B_2^8 + C_2^8 + A_1^8 + B_1^8 + C_1^8$

user 1 obtains $A_1^{[\theta_1]}(A_2, B_2, C_2)$ and $A_2^{[\theta_1]}(A_2, B_2, C_2)$, from which A_2 can be decoded as in the SJ scheme. Similarly, by eliminating A_2, B_2 and C_2 from the answers, user 2 can decode B_1 . Therefore, both users can recover their desired messages.

Privacy: We show that the delivery scheme is private from DB 1's viewpoint. First, by the privacy of the SJ scheme, DB 1 can neither determine which of the packets A_2, B_2 and C_2 is requested by user 1, nor determine which of the packets A_1, B_1 and C_1 is requested by user 2. Second, note that random permutations that are applied to the set of packets $\{A_1, B_1, C_1\}$ and $\{A_2, B_2, C_2\}$ are independent. These two aspects guarantee the privacy of θ with respect to DB 1. By symmetry, the scheme is also private from DB 1's viewpoint.

Performance: Since $D = 14$ bits are downloaded in total, the achieved load is $R = 7/8$. \diamond

B. General Achievable Scheme

For general K, K_u and N , we assume that $t = \frac{K_u M}{K} \in [1 : K_u]$. Each message is assumed to have $L = \binom{K_u}{t} N^K$ bits. The cache placement and delivery phases are described as follows.

1) *Cache Placement:* The MAN cache placement is applied over the message packets. In particular, each message W_k is split into $\binom{K_u}{t}$ disjoint and equal-sized packets, i.e., $W_k \triangleq \{W_{k,T} : T \subseteq [K_u], |T| = t\}, \forall k \in [K]$. Therefore, each packet consists of $\frac{L}{\binom{K_u}{t}} = N^K$ bits. User u then stores all the packets $W_{k,T}$ such that $u \in T$, i.e.,

$$Z_u = \{W_{k,T} : T \subseteq [K_u], |T| = t, u \in T, \forall k \in [K]\}, \quad (39)$$

for each $u \in [K_u]$. Therefore, each user stores $KL \frac{\binom{K_u-1}{t-1}}{\binom{K_u}{t}} = ML$ bits, satisfying the memory size constraint.

2) *Private Delivery:* Suppose the user demands are $\theta = (\theta_1, \dots, \theta_{K_u})$. We first construct $\binom{K_u}{t+1}$ different coded messages

$$\{X_S^{[\theta]} \triangleq (A_{1,S}^{[\theta]}, A_{2,S}^{[\theta]}, \dots, A_{N,S}^{[\theta]} : S \subseteq [K_u], |S| = t+1\}, \quad (40)$$

each of which being useful to a subset of $t+1$ users in S . The n -th component $A_{n,S}^{[\theta]}$ of $X_S^{[\theta]}$ represents the answer from DB n . For each S , the components of the coded message $X_S^{[\theta]}$ are constructed as

$$A_{n,S}^{[\theta]} = \sum_{u \in S} A_n^{[\theta_u]}(W_{1:K, S \setminus \{u\}}), \quad \forall n \in [N], \quad (41)$$

where $W_{1:K,S\setminus\{u\}} \triangleq (W_{1,S\setminus\{u\}}, \dots, W_{K,S\setminus\{u\}})$. The term $A_n^{[\theta_u]}(W_{1:K,S\setminus\{u\}})$ in the summation of (41) represents the answer from DB n in the SJ scheme for the single-user PIR problem when the messages are (First message, second message, \dots , K -th message) = $(W_{1,S\setminus\{u\}}, \dots, W_{K,S\setminus\{u\}})$ and user u demands $W_{\theta_u,S\setminus\{u\}}$. To preserve demand privacy, the users employ a random and uniform i.i.d. permutation (not known to the DBs) of the N^K bits of each packet $W_{\theta_u,S\setminus\{u\}}$. For different coded messages $X_S^{[\theta]}$, the set of the random bit permutations of the corresponding packets are also independent from each other. Moreover, the ordering of the randomly-permuted message bits in the query to the DBs is preserved as in the SJ scheme in the summation of (41). Next we prove that the proposed PD is both correct and private.

Correctness: We show that for any user $u \in [K_u]$, it can correctly recover its desired message W_{θ_u} from all the answers received. Since the packets $\{W_{k,T} : |T| = t, u \in T, \forall k \in [K]\}$ are already cached by user u , it needs to recover the packets $\{W_{\theta_u,T} : |T| = t, u \notin T\}$. For each $n \in [N], S \subseteq [K_u]$ such that $|S| = t + 1, u \in S$, we can write (41) as

$$A_{n,S}^{[\theta]} = A_n^{[\theta_u]}(W_{1:K,S\setminus\{u\}}) + \sum_{u' \in S\setminus\{u\}} A_n^{[\theta_{u'}]}(W_{1:K,S\setminus\{u'\}}), \quad (42)$$

from which user u can decode the desired term $A_n^{[\theta_u]}(W_{1:K,S\setminus\{u\}})$ since all the packets $\{W_{1:K,S\setminus\{u'\}} : u' \neq u\}$ are cached by user u because $u \in S\setminus\{u'\}$. Therefore, user u obtains a set of desired answers $\{A_n^{[\theta_u]}(W_{1:K,S\setminus\{u\}}) : \forall n \in [N]\}$ from which the desired packet $W_{\theta_u,S\setminus\{u\}}$ can be decoded due to the decodability of the SJ scheme. Going through all different S , user u can decode all the $\binom{K_u-1}{t}$ desired packets. As a result, user u can correctly recover its desired message W_{θ_u} .

Privacy: It can be seen that each $A_{n,S}^{[\theta]}, \forall n \in [N]$ of the coded message $X_S^{[\theta]}$ is independent of the demands of the users in S and that of the users in $[K_u]\setminus S$ from the viewpoint of each DB. The reason is explained as follows. For any user $u \in S$, the first term in (42), i.e., $A_n^{[\theta_u]}(W_{1:K,S\setminus\{u\}}), \forall n \in [N]$ is independent of θ_u by the privacy of the SJ scheme. Also, each term in the summation in (42) is independent of θ_u because for each $A_n^{[\theta_{u'}]}(W_{1:K,S\setminus\{u'\}})$, a set of random and independent permutations are employed to the bits of the set of packets $\{W_{k,S\setminus\{u'\}} : \forall k \in [K]\}$. Therefore, $A_{n,S}^{[\theta]}$ is independent of the demands of the users in S . Moreover, for S where $u \notin S$, due to the employment of the random and independent permutations, $A_{n,S}^{[\theta]}$ is independent of the demands of the users in $[K_u]\setminus S$. As a result, $A_{n,S}^{[\theta]}$ is independent of θ from DB n 's viewpoint for any $S \subseteq [K_u], |S| = t + 1$, which completes the proof of privacy.

Performance: By the SJ PIR scheme, each $X_S^{[\theta]}$ has $(1 + \dots + \frac{1}{N^{K-1}})N^K$ bits. Therefore, $D = \binom{K_u}{t+1} (1 + \dots + \frac{1}{N^{K-1}})N^K$. As a result, the achieved load is $R(M) = \frac{K_u-t}{t+1} (1 + \dots + \frac{1}{N^{K-1}})$.

VI. DISCUSSION: MUPIR WITH DISTINCT DEMANDS

In this section, we consider an interesting scenario where the users have distinct demands. Recall that R_d^* denotes the minimum load. We obtain the following theorem.

Theorem 3: For the cache-aided MuPIR problem with $K = 2$ messages, $K_u = 2$ users and $N = 2$ DBs, where the users demand distinct messages in a uniform manner, the optimal memory-load trade-off is characterized as

$$R_d^*(M) = \begin{cases} 2(1-M), & 0 \leq M \leq 1/3 \\ 5/3 - M, & 1/3 \leq M \leq 2/3 \\ \frac{3(2-M)}{4}, & 2/3 \leq M \leq 2 \end{cases} \quad (43)$$

Proof: For achievability, we show that the memory-load pairs $(1/3, 4/3)$ and $(2/3, 1)$ are achievable using the idea of CIA in Section VI-A. Together with the two trivial pairs $(0, 2)$ and $(2, 0)$, we obtain four corner points. By the memory-sharing among these corner points, the load of Theorem 3 can be achieved. For the converse, when $M \leq 1/3$, the load of $R_d(M) = 2(1-M)$ coincides with the caching bound without demand privacy and hence is optimal. When $M \geq 2/3$, the load $R_d(M) = \frac{3(2-M)}{4}$ is optimal since it coincides with the single-user cache-aided PIR bound of [9]. A novel converse bound is derived for the case $1/3 \leq M \leq 2/3$ to show the optimality of $R(M) = 5/3 - M$ when the users have distinct demands. ■

Remark 3: It can be seen that the load in (43) is lower than the one in (1) achieved by the CIA based scheme. Thus the optimal load under the constraint of distinct demands can be strictly lower than the optimal load without such constraint (See Fig. 2a). The reason is that, by removing the cases of identical demands, the decodability constraint is relaxed and therefore allows a lower achievable load.

A. Achievability

First, we consider the achievability of the memory-load pair $(1/3, 4/3)$. Assume that the users have distinct demands, i.e., $\theta = (\theta_1, \theta_2) \in \{(1, 2), (2, 1)\}$.

1) **Cache placement:** Assume that each message contains $L = 3$ bits, i.e., $W_1 = (a_1, a_2, a_3)$, $W_2 = (b_1, b_2, b_3)$. The cache placement is $Z_1 = \{a_1 + b_1\}$, $Z_2 = \{a_2 + b_2\}$ and therefore $M = 1/3$.

2) **Private delivery:** Let $A_{1,1}$ and $A_{1,2}$ be two different answers of DB 1, and let $A_{2,1}$ and $A_{2,2}$ be two different answers of DB 2. The answers are

$$\begin{aligned} A_{1,1} &= (a_3, b_1 + b_2 + b_3), \\ A_{1,2} &= (a_1 + a_2 + a_3, b_3), \\ A_{2,1} &= (a_2 + a_3, b_2 + b_3), \\ A_{2,2} &= (a_1 + a_3, b_1 + b_3). \end{aligned} \quad (44)$$

The delivery scheme is that the users randomly choose $A_{1,1}$ or $A_{1,2}$ to request from DB 1 with equal probabilities. We then consider the following two cases. When $(\theta_1, \theta_2) = (1, 2)$, if $A_{1,1}$ is chosen, then go to DB 2 to download $A_{2,1}$. Otherwise, if $A_{1,2}$ is chosen, go to DB 2 to download $A_{2,2}$. When $(\theta_1, \theta_2) = (2, 1)$, if $A_{1,1}$ is chosen, then go to DB 2 to

download $A_{2,2}$. Otherwise if $A_{1,2}$ is chosen, go to DB 2 to download $A_{2,1}$.

Correctness: One can check that $(A_{1,1}, A_{2,1}, Z_1) \rightarrow W_1$ (meaning that W_1 can be recovered from $A_{1,1}, A_{2,1}$ and Z_1), $(A_{1,1}, A_{2,1}, Z_2) \rightarrow W_2$, $(A_{1,2}, A_{2,2}, Z_1) \rightarrow W_1$, $(A_{1,2}, A_{2,2}, Z_2) \rightarrow W_2$, $(A_{1,1}, A_{2,2}, Z_1) \rightarrow W_2$, $(A_{1,1}, A_{2,2}, Z_2) \rightarrow W_1$, $(A_{1,2}, A_{2,1}, Z_1) \rightarrow W_2$, and $(A_{1,2}, A_{2,1}, Z_2) \rightarrow W_1$. Therefore, both users can decode their desired messages.

Privacy: Note that the answer from DB 1 is equally likely to be $A_{1,1}$ or $A_{1,2}$, and the answer from DB 2 is also equally likely to be $A_{2,1}$ or $A_{2,2}$. Therefore, we have

$$P(\theta = (1, 2)) = P((A_{1,1}, A_{2,1})) + P((A_{1,2}, A_{2,2})) = 1/2, \quad (45a)$$

$$P(\theta = (2, 1)) = P((A_{1,1}, A_{2,2})) + P((A_{1,2}, A_{2,1})) = 1/2. \quad (45b)$$

such that the privacy constraint (3) is satisfied (for distinct demands). Since $D = 4$ bits are downloaded, the achieved load is $R_d = 4/3$.

Second, we consider the achievability of $(2/3, 1)$. Let $W_1 = (a_1, a_2, a_3)$, $W_2 = (b_1, b_2, b_3)$. The cache placement is $Z_1 = \{a_1, b_1\}$, $Z_2 = \{a_2, b_2\}$ and the answers are

$$\begin{aligned} A_{1,1} &= (a_3 + b_3 + b_1 + b_2), \\ A_{1,2} &= (a_3 + b_3 + a_1 + a_2), \\ A_{2,1} &= (a_2 + a_3, b_2 + b_3), \\ A_{2,2} &= (a_1 + a_3, b_1 + b_3). \end{aligned} \quad (46)$$

The delivery scheme is similarly to that of $(1/3, 4/3)$. The correctness of this scheme can be easily verified. The privacy argument is similar to the previous case, i.e., from each DB's viewpoint, the demand vector θ is equally likely to be $(1, 2)$ or $(2, 1)$. Since $D = 3$ bits are downloaded in total, the achieved load is $R_d = 1$.

B. Converse

The converse consists of three piece-wise linear segments corresponding to different cache memory regimes $M \in [0, 1/3]$, $[1/3, 2/3]$ and $[2/3, 2]$. We prove the converse for each segment respectively. When $M \in [0, 1/3]$, the cut-set bound $R \geq 2(1 - M)$ without privacy constraint is tight. Since increasing the number of users while preserving the demand privacy can only possibly increase the load, the single-user cache-aided PIR converse given in [9] is also a converse for our considered MuPIR problem. This gives a bound $R_d^*(M) \geq \frac{3(2-M)}{4}$ for $M \in [2/3, 2]$. When $M \in [1/3, 2/3]$, we develop a new converse $R_d(M) \geq 5/3 - M$ as follows.

Let $A_{1,1} = A_{1,1}^{[(1,2)]} = A_{1,1}^{[(2,1)]}$ be an answer of DB 1 and let $A_{2,1} = A_{2,1}^{[(1,2)]} = A_{2,1}^{[(2,1)]}$ be an answer of DB 2. It is clear that the message W_1 can be recovered from $\{A_{1,1}^{[(1,2)]}, A_{2,1}^{[(1,2)]}, Z_1\}$ while W_2 can be recovered from $\{A_{1,1}^{[(1,2)]}, A_{2,1}^{[(1,2)]}, Z_2\}$ for which we use a shorthand notation as $(A_{1,1}^{[(1,2)]}, A_{2,1}^{[(1,2)]}, Z_1) \rightarrow W_1$, $(A_{1,1}^{[(1,2)]}, A_{2,1}^{[(1,2)]}, Z_2) \rightarrow W_2$. For privacy with respect to DB 1, there must exist another answer $A_{2,2}^{[(2,1)]}$ of DB 2 such that the demands

$\theta = (2, 1)$ can be satisfied, i.e., $(A_{1,1}^{[(2,1)]}, A_{2,2}^{[(2,1)]}, Z_1) \rightarrow W_2$ and $(A_{1,1}^{[(2,1)]}, A_{2,2}^{[(2,1)]}, Z_2) \rightarrow W_1$. Also, for privacy with respect to DB 2, there must exist another answer $A_{1,2}^{[(2,1)]}$ of DB 1 such that $(A_{1,2}^{[(2,1)]}, A_{2,1}^{[(2,1)]}, Z_1) \rightarrow W_2$ and $(A_{1,2}^{[(2,1)]}, A_{2,1}^{[(2,1)]}, Z_2) \rightarrow W_1$. Note that $R_d = \frac{H(A_{1,i}) + H(A_{2,j})}{L}$ for any index pair $(i, j) \in \{(1, 1), (1, 2), (2, 1)\}$ because the load does not depend on the demands. Denote $X_{i,j,k}^{[\theta]} \triangleq (A_{1,i}^{[\theta]}, A_{2,j}^{[\theta]}, Z_k)$, $\forall (i, j, k) \in \{(1, 1, 1), (1, 2, 2), (2, 1, 2)\}$. Then

$$\begin{aligned} & 3R_d(M)L + 3ML \\ & \geq H(X_{1,1,1}^{[(1,2)]}) + H(X_{2,1,2}^{[(2,1)]}) + H(X_{1,2,2}^{[(2,1)]}) \\ & \stackrel{(a)}{=} 3L + H(X_{1,1,1}^{[(1,2)]}|W_1) + H(X_{2,1,2}^{[(2,1)]}|W_1) \\ & \quad + H(X_{1,2,2}^{[(2,1)]}|W_1) \\ & \stackrel{(b)}{\geq} 3L + H(X_{1,1,1}^{[(1,2)]}|W_1) + H(Z_2|W_1) \\ & \quad + H(A_{2,1}^{[(2,1)]}|W_1, Z_2) \\ & \quad + H(X_{1,2,2}^{[(2,1)]}|W_1) \\ & \geq 3L + H(X_{1,1,1}^{[(1,2)]}, Z_2|W_1) + H(A_{2,1}^{[(2,1)]}|W_1, Z_2) \\ & \quad + H(X_{1,2,2}^{[(2,1)]}|W_1) \\ & \stackrel{(c)}{=} 4L + H(A_{2,1}^{[(2,1)]}|W_1, Z_2) + H(X_{1,2,2}^{[(2,1)]}|W_1) \\ & \geq 4L + H(A_{2,1}^{[(2,1)]}|W_1, Z_2) + H(A_{1,1}^{[(2,1)]}|W_1, Z_2) \\ & \quad + H(Z_2|W_1) \\ & \geq 4L + H(A_{1,1}^{[(2,1)]}, A_{2,1}^{[(2,1)]}|W_1, Z_2) + H(Z_2|W_1) \\ & = 4L + H(A_{1,1}^{[(2,1)]}, A_{2,1}^{[(2,1)]}, Z_2|W_1) \\ & = 4L + H(A_{1,1}^{[(1,2)]}, A_{2,1}^{[(1,2)]}, Z_2|W_1) \\ & = 5L \end{aligned} \quad (47)$$

where (a) is due to $X_{1,1,1}^{[(1,2)]} \rightarrow W_1$, $X_{2,1,2}^{[(2,1)]} \rightarrow W_1$ and $X_{1,2,2}^{[(2,1)]} \rightarrow W_1$; In (b) we used the chain rule and non-negativity of mutual information, i.e., $H(X_{2,1,2}^{[(2,1)]}|W_1) = H(Z_2|W_1) + H(A_{2,1}^{[(2,1)]}|W_1, Z_2) + H(X_{1,2,2}^{[(2,1)]}|W_1, Z_2, A_{2,1}^{[(2,1)]}) \geq H(A_{2,1}^{[(2,1)]}|W_1, Z_2) + H(Z_2|W_1)$; (c) is because both W_1 and W_2 can be decoded from $X_{1,1,1}^{[(1,2)]}$ and Z_2 . (47) implies $R_d(M) \geq 5/3 - M$, which completes the converse proof of Theorem 3.

VII. CONCLUSION

In this paper, we introduced the problem of cache-aided multiuser Private Information Retrieval (MuPIR), which generalizes the single-user cache-aided PIR problem to the case of multiple users. We provided achievability for the MuPIR problem with two messages, two users and arbitrary number of databases utilizing the novel idea of cache-aided Interference Alignment (CIA). The proposed scheme is shown to be optimal when the cache placement is uncoded. For general system parameters, inspired by both single-user PIR and coded caching, we proposed a product design which is order optimal within a factor of 8. Moreover, when the user's demands are constrained to be distinct, the optimal memory-load trade-off is characterized for a system with two messages, two

users and two databases. Due to the strong connection to both PIR and coded caching, our result on the cache-aided MuPIR problem provides useful insights into understanding the role of side information (i.e., cache) in multiuser and multi-message PIR. Besides the proposed achievability and converse results, the cache-aided MuPIR problem still remains open for arbitrary system parameters in terms of the optimal memory-load trade-off. For example, utilizing the idea of CIA, we expect more achievability results to come. Also, based on the well-established converse results of coded caching and PIR, a systematic approach to characterize the converse is needed.

APPENDIX A COMPUTER-AIDED CONVERSE

In this section, we provide a brief description of the open source toolbox CAI developed by [29], which conducts a computer-aided investigation on the fundamental limits of information systems. By utilizing the linear programming (LP) framework involving the Shannon-type inequalities, the CAI solver is able to read a problem description file which defines the problem-specific random variables and their dependency. It then computes a bound for a given linear combination of information measures, and provides the value of information measures at the optimal solution or a proof as a weighted sum of known information inequalities. This toolbox was shown rather effective in the problems of distributed storage, coded caching and PIR [34]–[36].

We next show how to use the CAI prover to prove the optimality of the proposed CIA based scheme for $K = K_u = 2$ and $N = 2, 3$. Due to limitation of space, we only present the case for $N = 2$. For brevity, we use a different notation from the main paper here.

Sketch of the Computer-Aided Proof

For the $K = K_u = N = 2$ cache-aided MuPIR problem, let f denote an answer of DB 1. By demand privacy, there must exist four different configurations of the DB 2 answer, denoted by g_1, g_2, g_3 and g_4 , such that the following decodability condition can be satisfied: $(f, g_1, Z_1) \rightarrow W_1$ (meaning that W_1 can be recovered from f, g_1 and Z_1), $(f, g_1, Z_2) \rightarrow W_1$, $(f, g_2, Z_1) \rightarrow W_1$, $(f, g_2, Z_2) \rightarrow W_2$, $(f, g_3, Z_1) \rightarrow W_2$, $(f, g_3, Z_2) \rightarrow W_1$, $(f, g_4, Z_1) \rightarrow W_2$, and $(f, g_4, Z_2) \rightarrow W_2$. The following privacy condition

$$H(g_i|S) = H(g_j|S), \forall i, j \in [4], \forall S \subseteq \{Z_1, Z_2, W_1, W_2\} \quad (48)$$

also needs to be satisfied. (48) implies that the four different configurations of DB 2's answer have identical entropy after removing the information S available to DB 2, i.e., these configurations are indistinguishable from DB 2's viewpoint. We also have the symmetry condition $H(f) = H(g_i)$, $\forall i \in [4]$ and the message and cache memory size constraints $H(W_1) = H(W_2) = 1, H(Z_1) = H(Z_2) = M$. Putting the above constraints as an input file to the CAI prover with LP objective $5R + 6M$, it produces the following points: (0.000000, 2.000000), (0.250000, 1.500000),

(0.666667, 1.000000), (2.000000, 0.000000), which coincides with the four achievable corner points by the CIA based scheme. This proves the optimality of the proposed scheme. For $N = 3$, a similar approach can be used.

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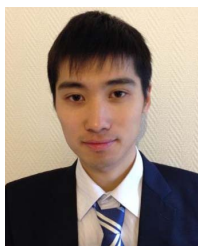
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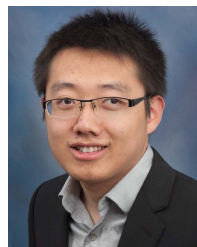
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